CISTER - Research Center in Real-Time & Embedded Computing Systems

Energy Efficient Mapping of Mixed Criticality Applications on Unrelated Heterogeneous Multicore Platforms

Muhammad Ali Awan, Damien Masson*, Eduardo Tovar

CISTER, ISEP, Polytechnic Institutue of Porto, Portugal *Universite Paris-Est, Paris, France





Outline

- Motivation and challenges
- System model
- Preliminaries
- Proposed allocation heuristics
- Evaluation
- Conclusions and future directions

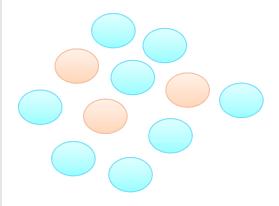
Motivation and challenges (1)

- Modern real-time applications are becoming increasingly complex
 - More than on criticality levels
 - Level of assurance against failure
 - High criticality applications are vital
 - Faults in low criticality tasks are tolerable
- Applications with different criticality levels were hosted in different components
- Trend towards deploying applications with different criticality levels on a same platform
 - Pessimistic WCET estimates
 - Inefficient resource utilisation

Motivation and challenges (2)

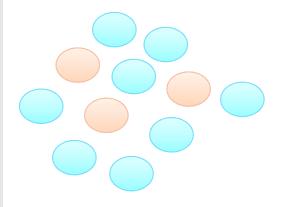
- Multicore platforms
 - Extensive computing resources
- Heterogeneous multicore
 - Diverse computing capabilities
 - Perform specific functions efficiently
 - Mapping of mixed criticality applications on a heterogeneous multicore platform is a non-trivial exercise
- Energy is important
 - Size
 - Weight
 - Thermal issue
 - Packing
 - Cost

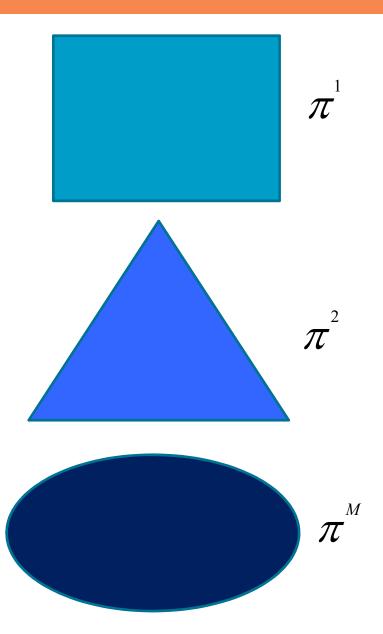
Mixed criticality applications

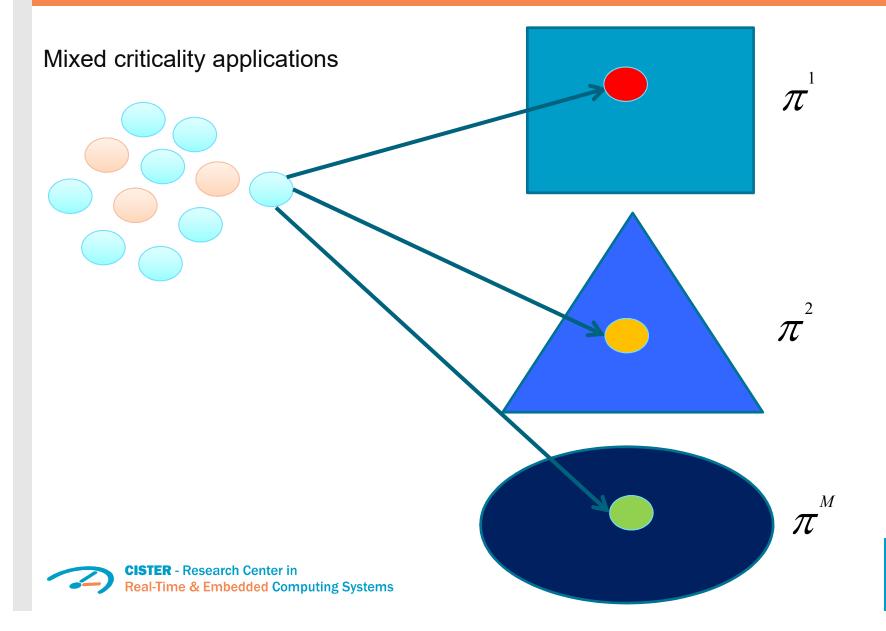




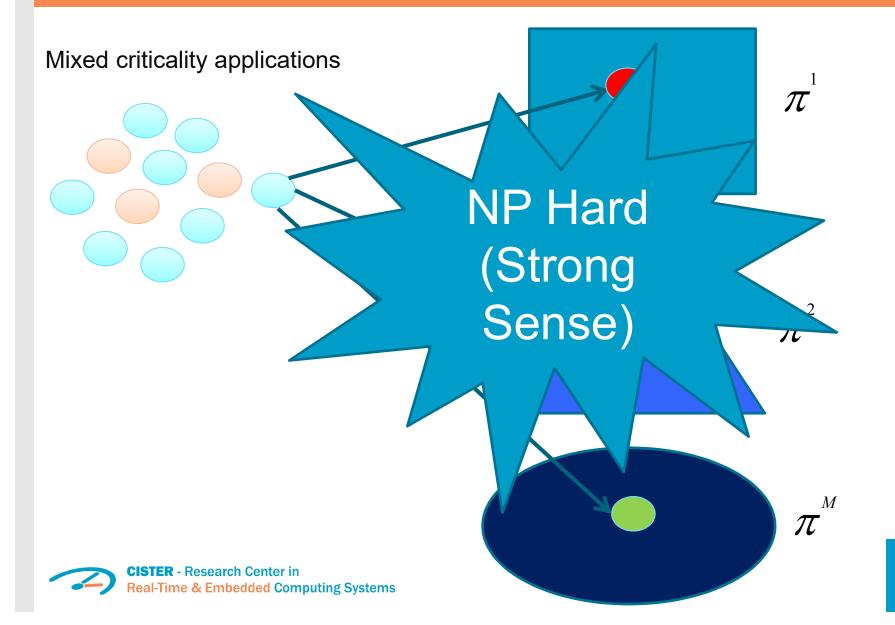
Mixed criticality applications

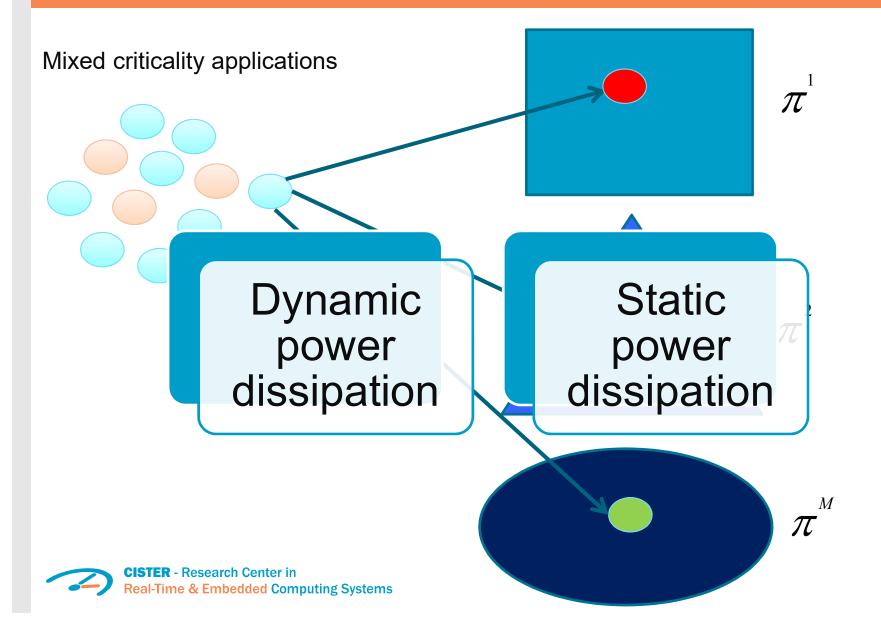


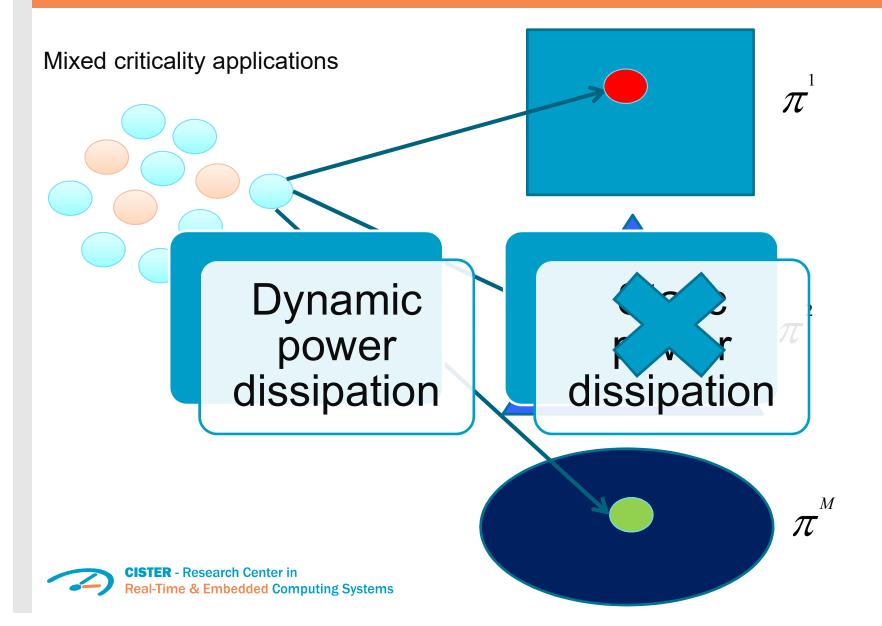




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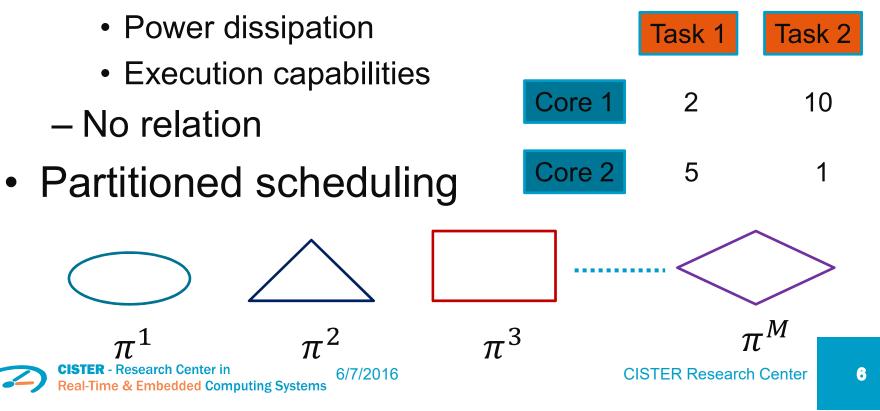






System model

- Unrelated heterogeneous multicore platform
 - Each core has different characteristics



Vestal's model

- Proposed by Steve Vestal
- Dual criticality
 - Two mode of operation
 - L-mode and H-mode
- System starts in L-mode and transition to H-mode in case of any abnormal behavior
 - Violations of parameters in L-mode
 - Overrun
 - Memory accesses etc

- Independent tasks
- Each task has either high or low criticality

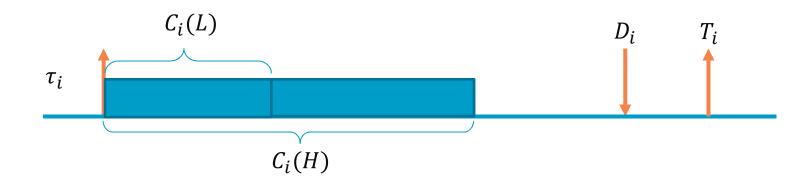
- Independent tasks
- Each task has either high or low criticality



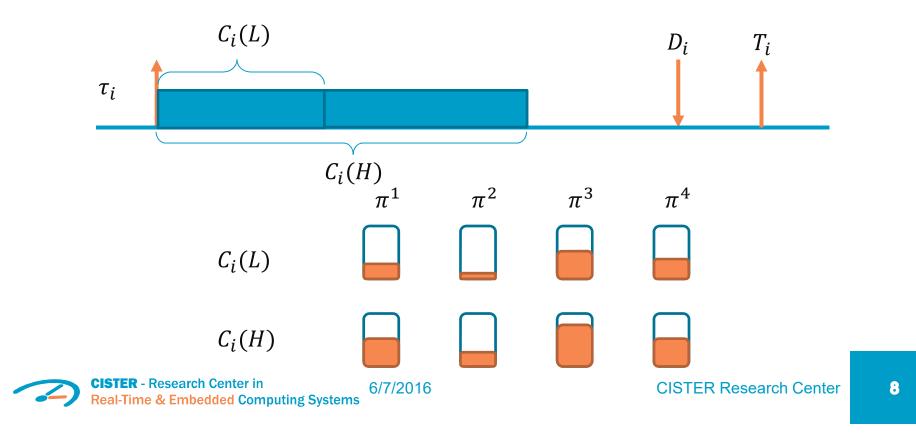
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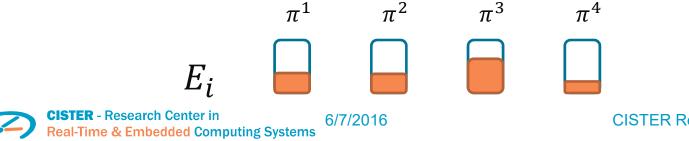


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- Each task has either high or low criticality



Energy model

- Energy is not only a function of WCET
 - Depends on the characteristic of the core
 - Set of instruction / parts of core used
 - Two application with similar WCET may have different energy consumption
- Measurement based approach (Snowdon)
 - Incorporates effect of different system resources (cache and memory etc)
- Energy in L-mode of operation



Schedulability analysis

- Mixed criticality schedulability analysis proposed by Ekberg and Yi
 - Ensures schedulability both in low and high mode of operation
 - Demand bound function based analysis
 - Shortens deadline for L-mode of operation
 - Independent scaling of deadlines
 - Ensures schedulability in transition phase
 - Valid for constrained deadline model

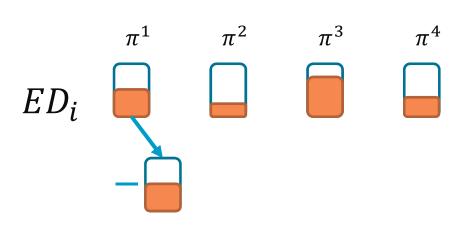
Energy density $(ED_i) = \frac{E_i}{T_i}$



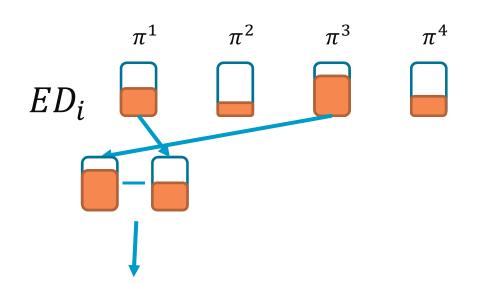
Energy density $(ED_i) = \frac{E_i}{T_i}$ $ED_i = \pi^2 \pi^3 \pi^4$



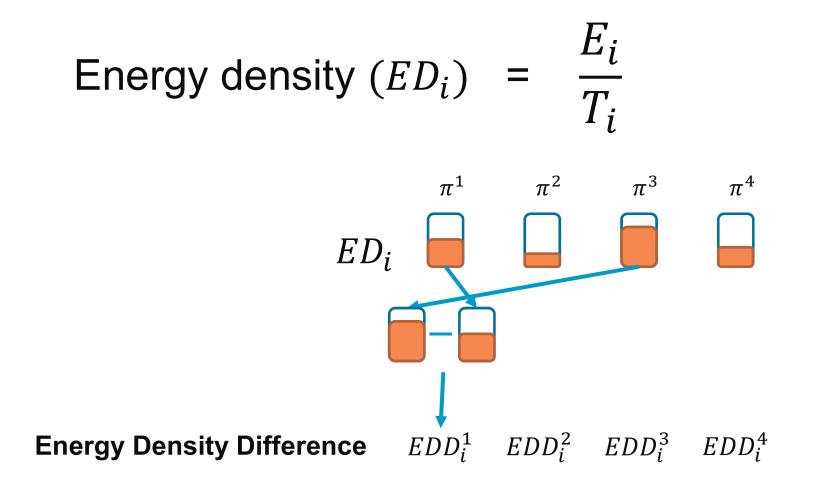
Energy density $(ED_i) = \frac{E_i}{T_i}$



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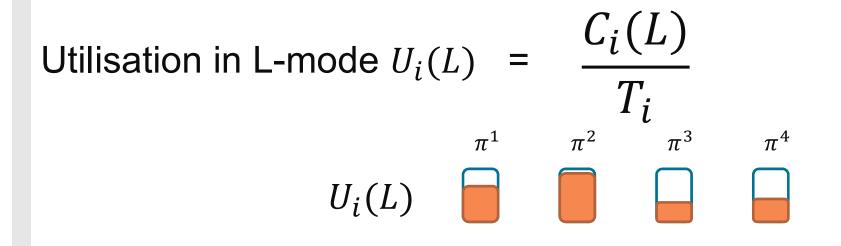




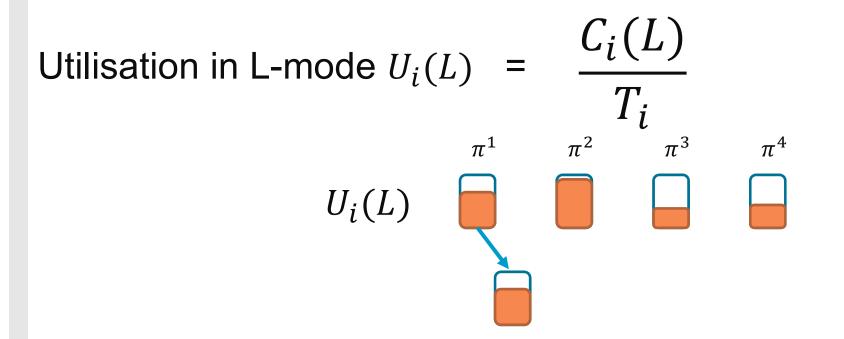
Utilisation in L-mode $U_i(L) = \frac{C_i(L)}{T_i}$



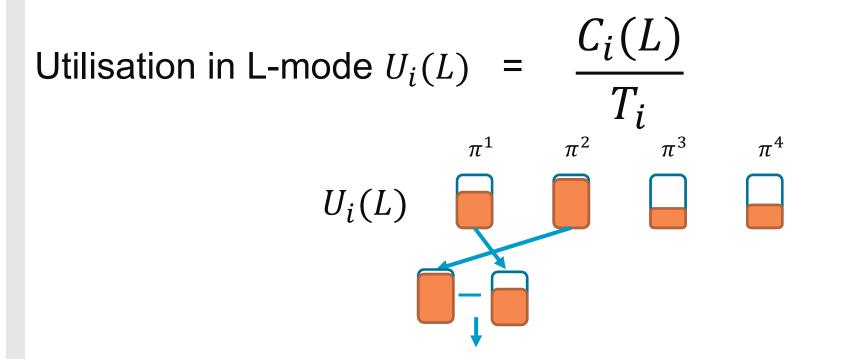




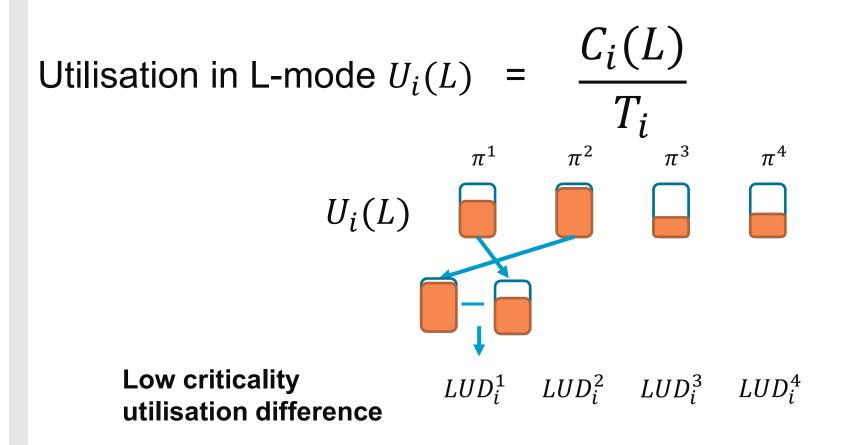




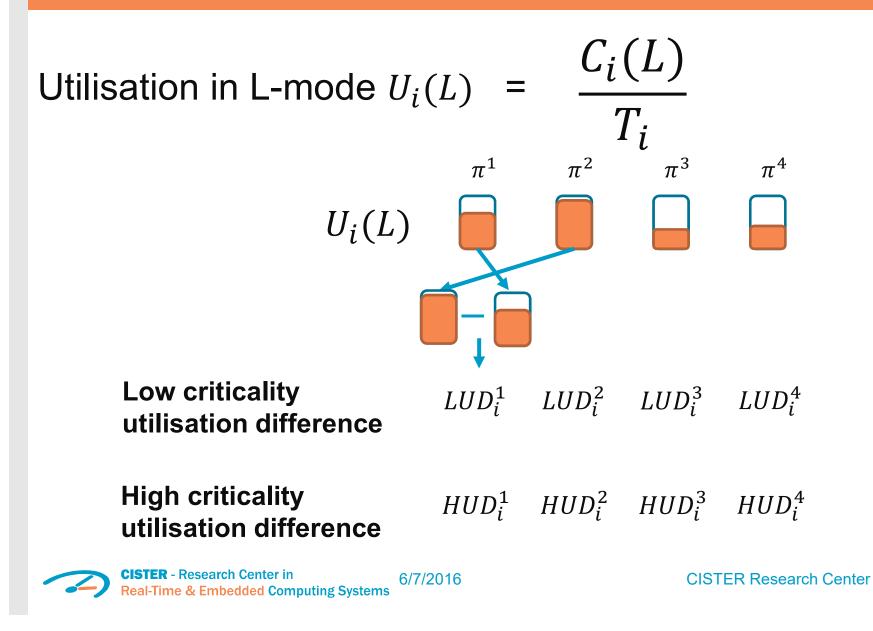










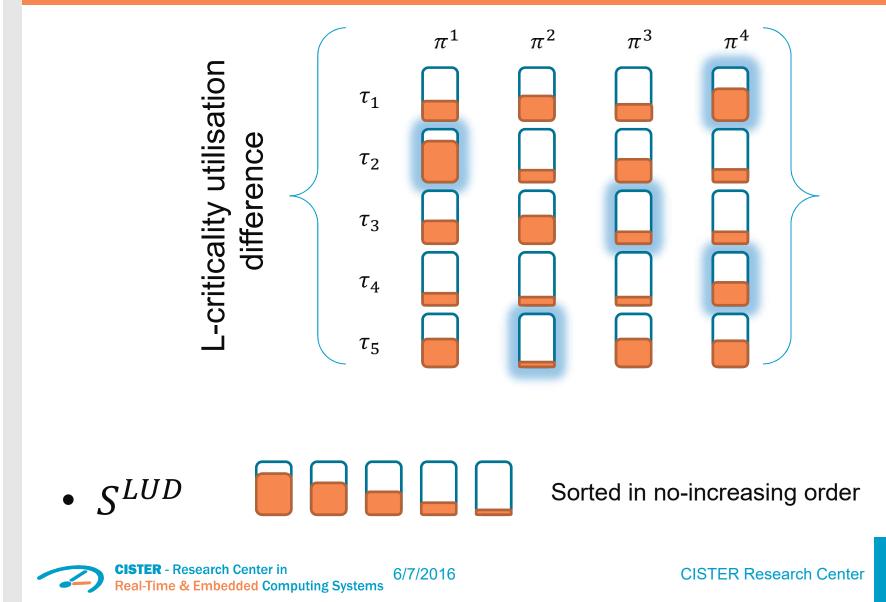


- Favourite core
 - Best option w.r.t any parameter
- Least preferred core
 - Worst option w.r.t any parameter

6/7/2016

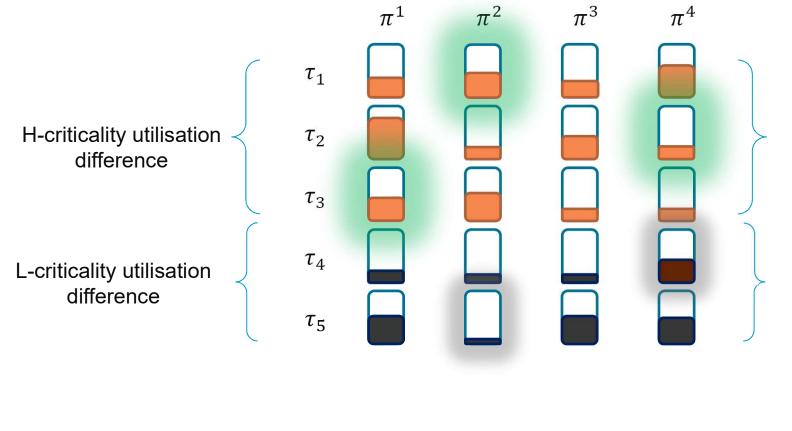
- Parameters
 - Energy consumption
 - L-mode utilisation
 - H-mode utilisation

Density difference List (1)



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Density difference List (2)

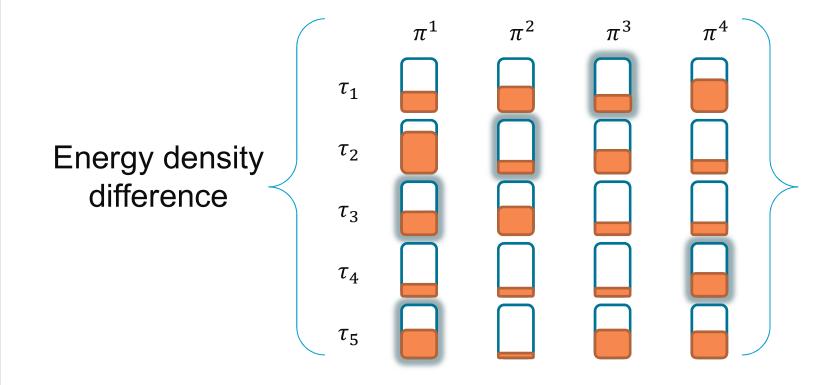




□ Sorted list (non-increasing)

- □ Criticality first
- Density difference

Density difference List (3)





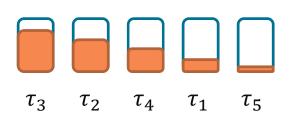
CISTER - Research Center in Real-Time & Embedded Computing Systems 6/7/2016 Sorted in no-increasing order of energy density difference

Improved least loss energy density algorithm (ILLED)

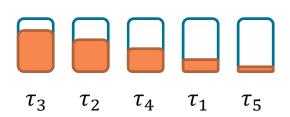
- Input: Any density difference list
 - $(S^{ED} \text{ or } S^{LUD} \text{ or } S^{HUD})$
 - Sorted in non-increasing order

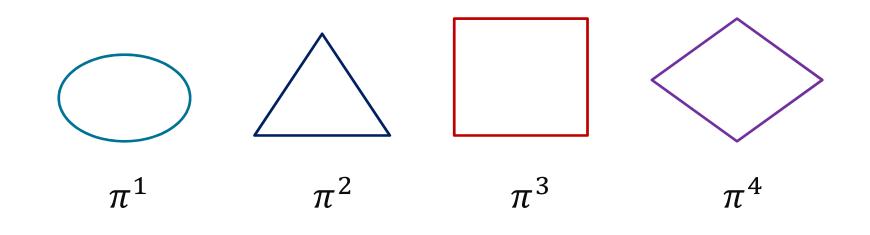
Improved least loss energy density algorithm (ILLED)

- Input: Any density difference list
 - $(S^{EDD} \text{ or } S^{LUD} \text{ or } S^{HUD})$
 - Sorted in non-increasing order

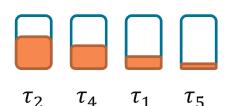


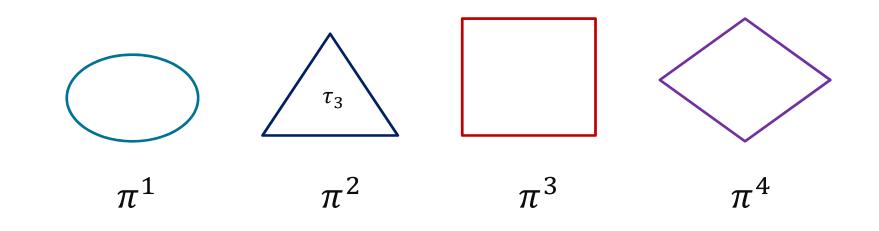
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 - Sorted in non-increasing order



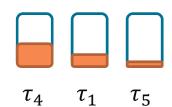


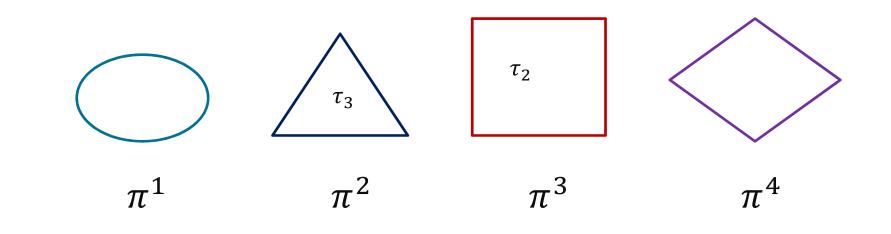
- Input: Any density difference list
 - $(S^{EDD} \text{ or } S^{LU} \text{ or } S^{HUD})$
 - Sorted in non-increasing order

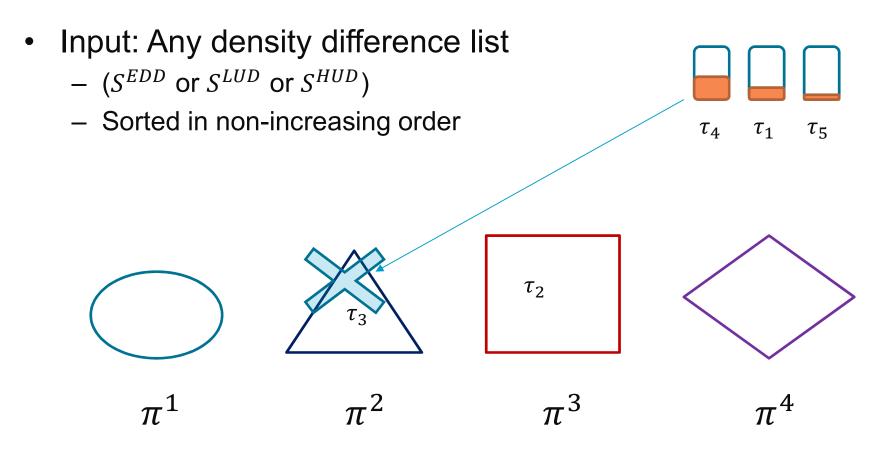




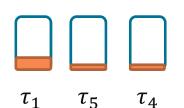
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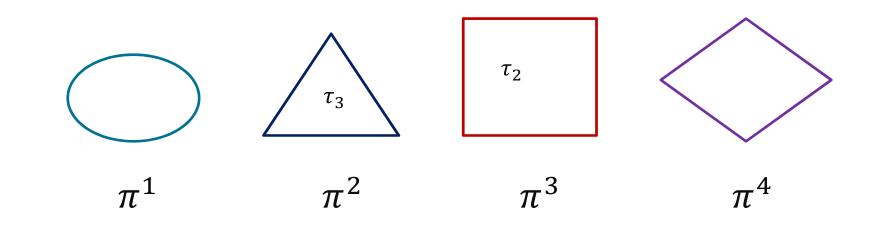




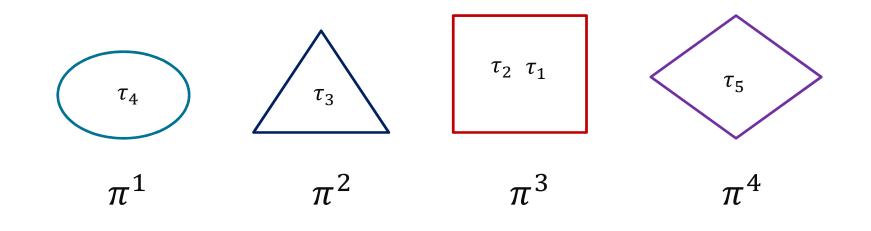


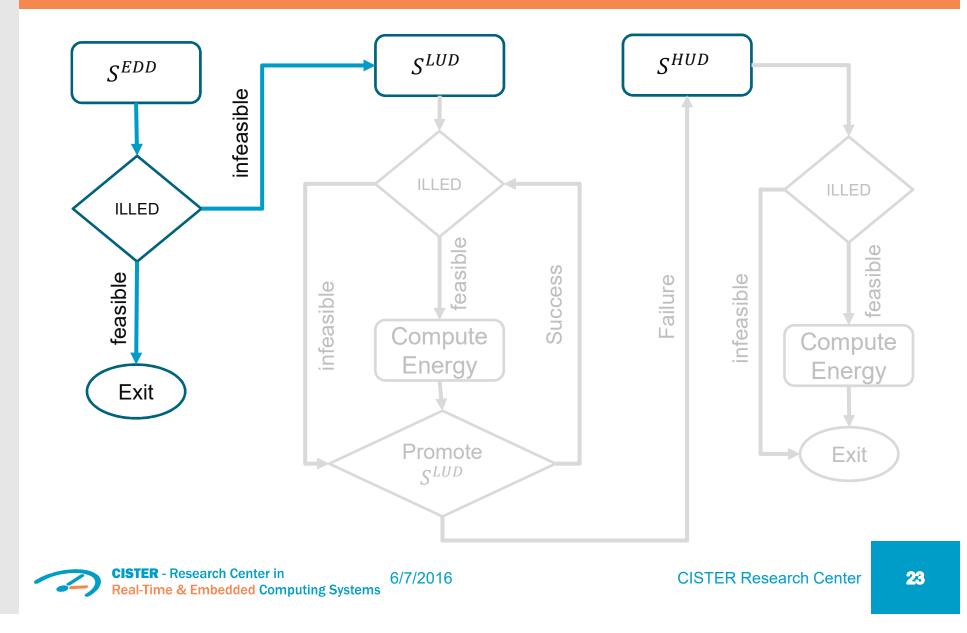
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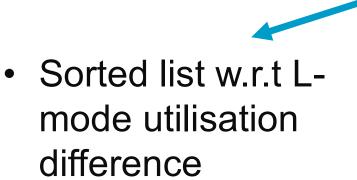




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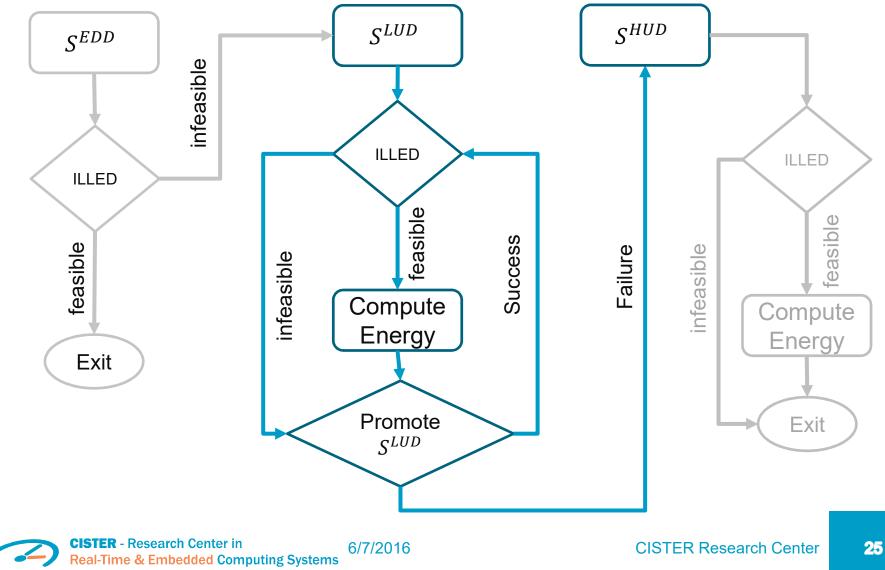


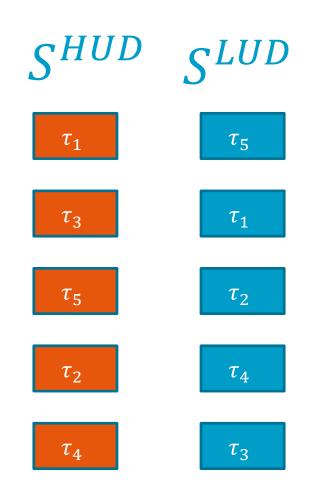




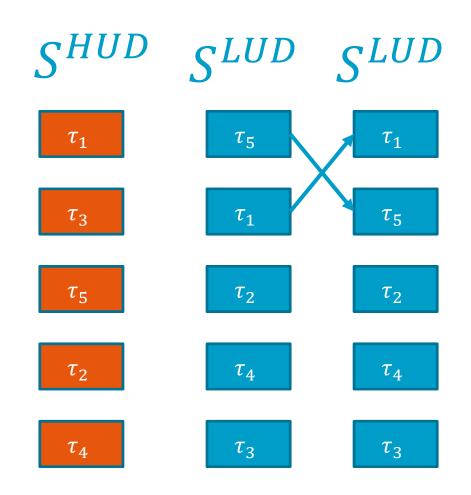
 Improves L-mode schedulability

- Sorted list w.r.t H and L-mode utilisation difference
 - Gives preference to H-criticality tasks
 - Improves H-mode schedulability

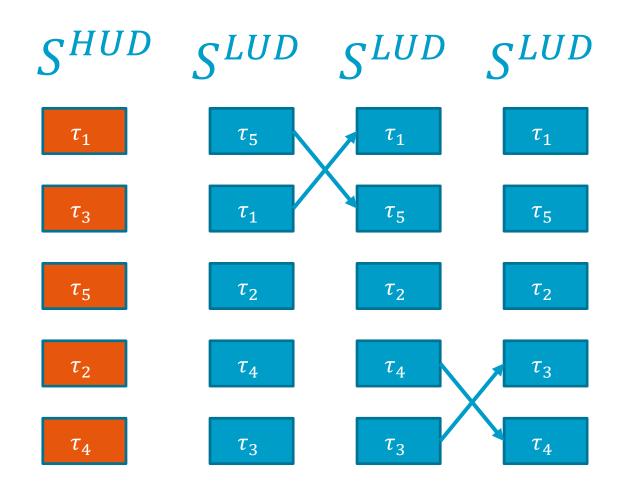


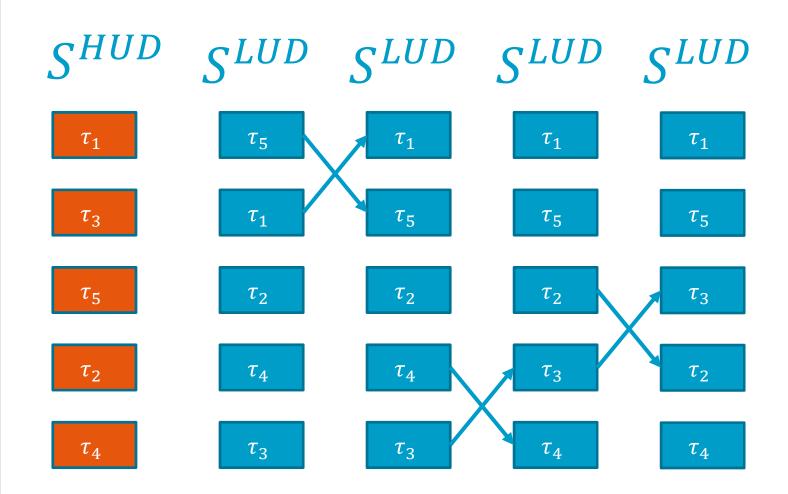


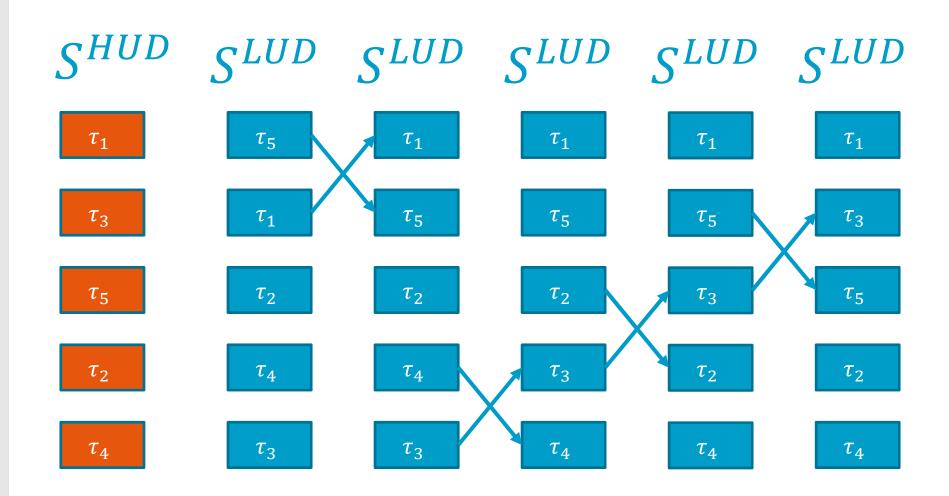
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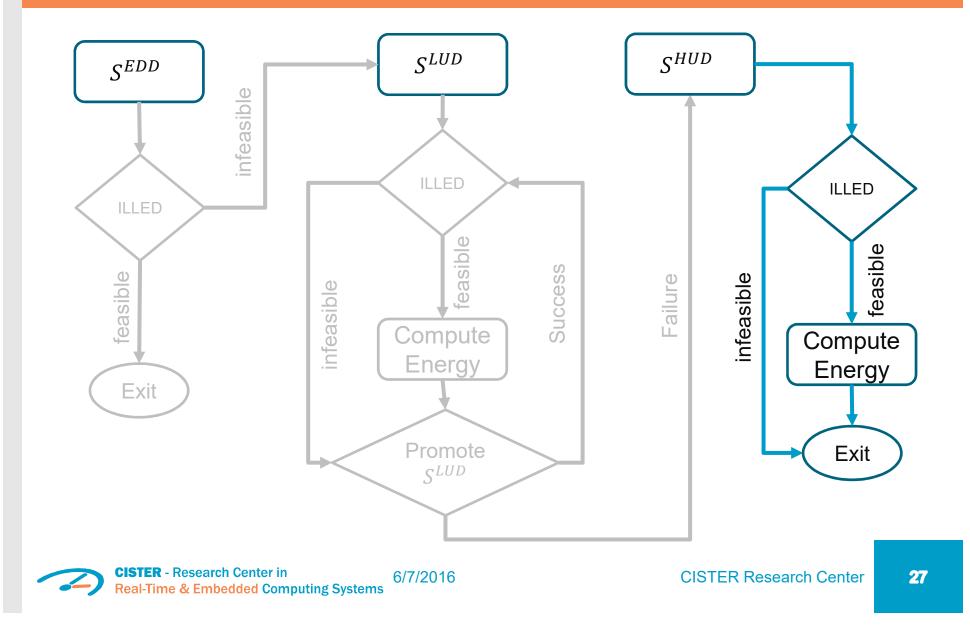












Complexity analysis

- Worst-case scenario
 - $-S^{EDD}$ is not feasible
 - All tasks are H-criticality tasks
 - $-S^{LUD}$ is in reverse order of S^{HUD}
- Iterations of ILLED = $\frac{n^2 n + 4}{2}$
- ILLED has a complexity of $O(n \times M)$
 - i.e., it performs $n \times M$ Ekberg and Yi analysis
- In total, $\frac{(n^3 n^2 + 4n)m}{2}$ Ekberg and Yi analysis

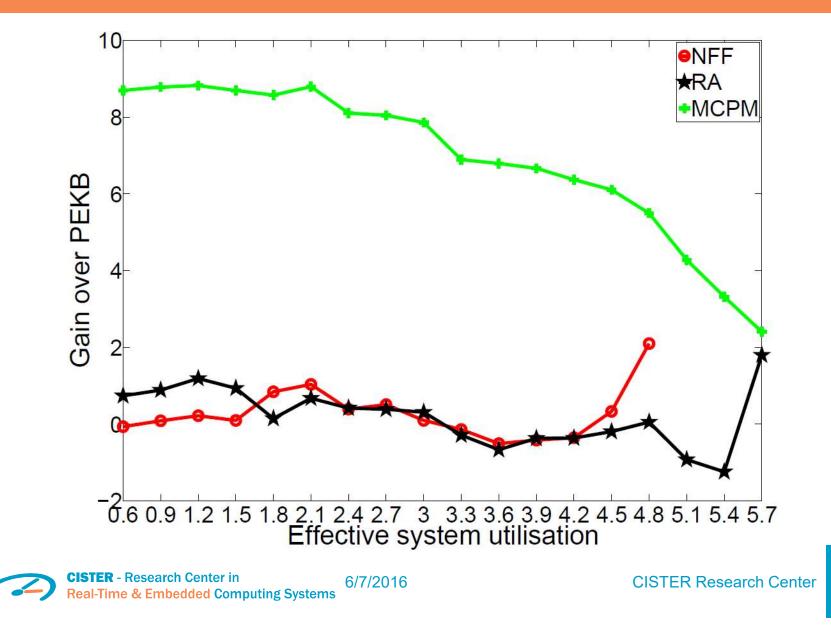
Experimental Setup

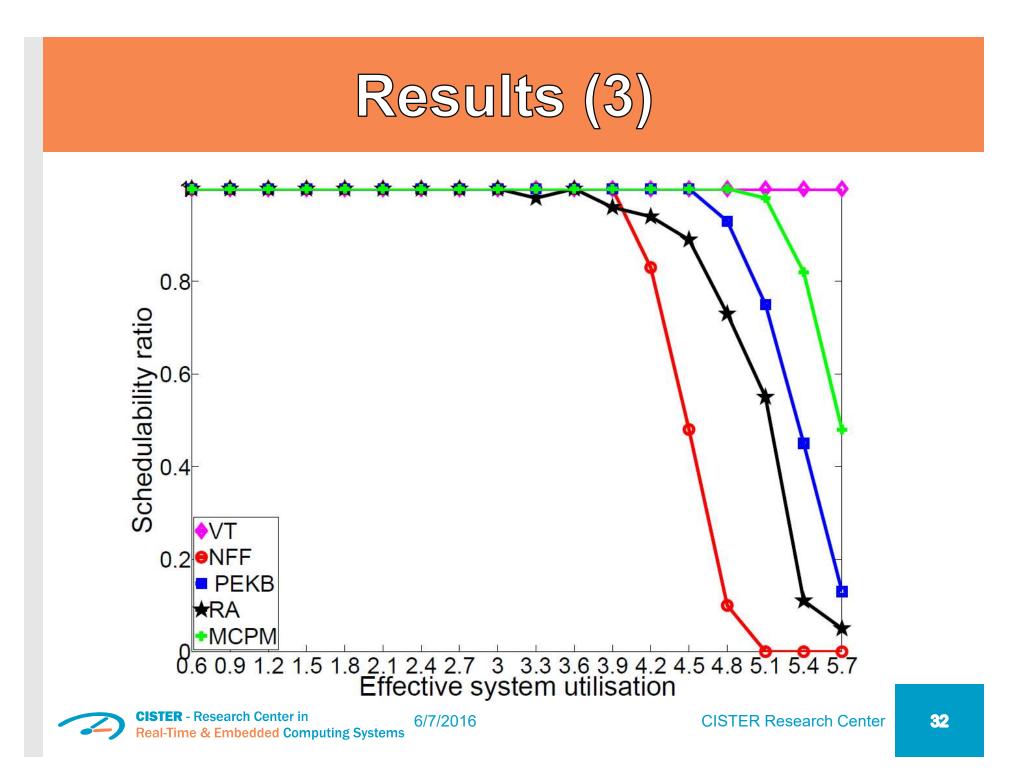
- Heterogeneous multicore platform
 FreeScale PowerQUICC III (MPC8536)
- SPARTS simulator
 - Extended for Mixed criticality systems
 - Synthetic workload
 - Utilisation (Uunifast-discard)
 - Periods (log-uniform distribution)
 - Deadlines (Implicit deadlines)
 - H-mode WCET (transfer function)

Results (1)

- Compared algorithms
 - Naïve first fit (NFF) \rightarrow FF, $C_i(L)$ and $C_i(H)$
 - Partitioned Ekberg (PEKB) → FF and Ekberg and Yi analysis
 - Random allocation (RA) → random and Ekberg and Yi analysis
 - Mixed criticality power management allocation (MCPM) → our proposed heuristics







Results (4)

Metric	H-Mode WCET multiplier	Number of cores	Number of tasks	Percentage of high criticality tasks	Variability in WCET, and energy
Energy gain	9,12%	12.01%	8.91%	8.93%	23.8%
Schedulability difference	44%	43%	44%	63%	52%
S ^{EDD} feasible	88.35%	88.68%	88.25%	86.12%	89.52%



Conclusions and future directions

- Energy-aware task-to-core allocation
 - Heterogeneous multicore platform
 - Mixed criticality applications
- More realistic power model
- Extensive simulations show substantial gains
- Can be used for other multi objective optimisation problems
- Possible extensions:
 - Multi criticality level model
 - Integrating the effect of I/O devices

Questions and Comments



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