

P-SOCRATES

Parallel Software Framework for Time-Critical many-core Systems

High-performance parallelization of real-time applications

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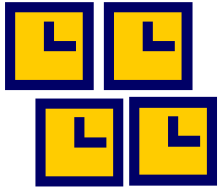
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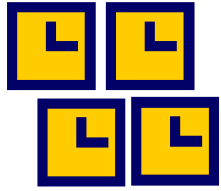


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Outline

- P-SOCRATES at a glance
- Motivation and Vision
- Technical Approach
- Timing Analysis Methodology
- The **UpScale** SDK
- Conclusions



Quick fact sheet

- **P-SOCRATES: Parallel SOftware framework for time-CRITICAL mAny-core sysTEmS**
- Three-year FP7 STREP project (Oct-2013, Dec-2016)
- **Website:** www.p-socrates.eu
- Budget: 3.6 M€
- Partners

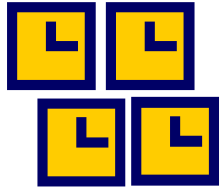


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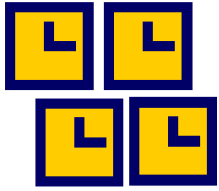
Industrial Advisory Board

- Review and prioritize requirements, ensure that the project is kept on focus, analyze and validate the results
- Members:



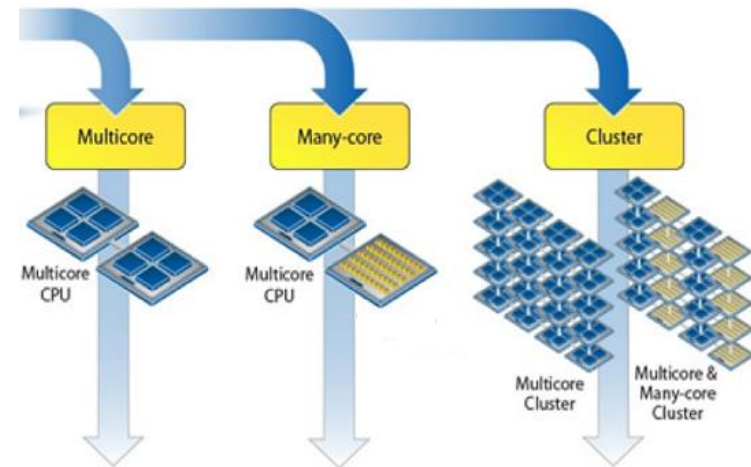
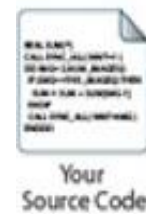
City of Bratislava

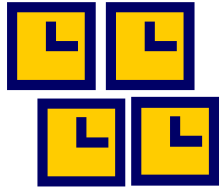




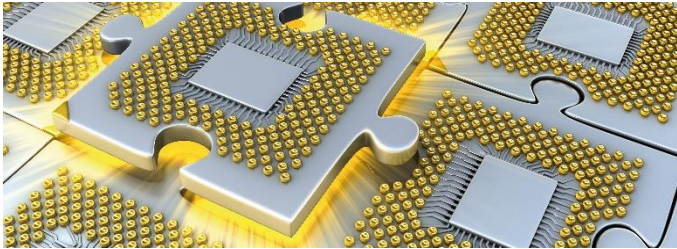
Motivation

- New applications challenge the performance capabilities of hardware platforms by crossing the boundaries of computing domains
 - Demand of increased **performance with guaranteed processing times**
 - Demands can only be met by **advanced parallel computing platforms**
 - **Programmability** of parallel platforms is a **major challenge**
 - Need to integrate **parallel programming models** in embedded systems





Vision



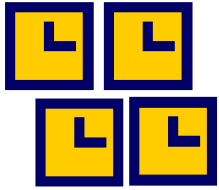
next-generation embedded
many-core accelerators



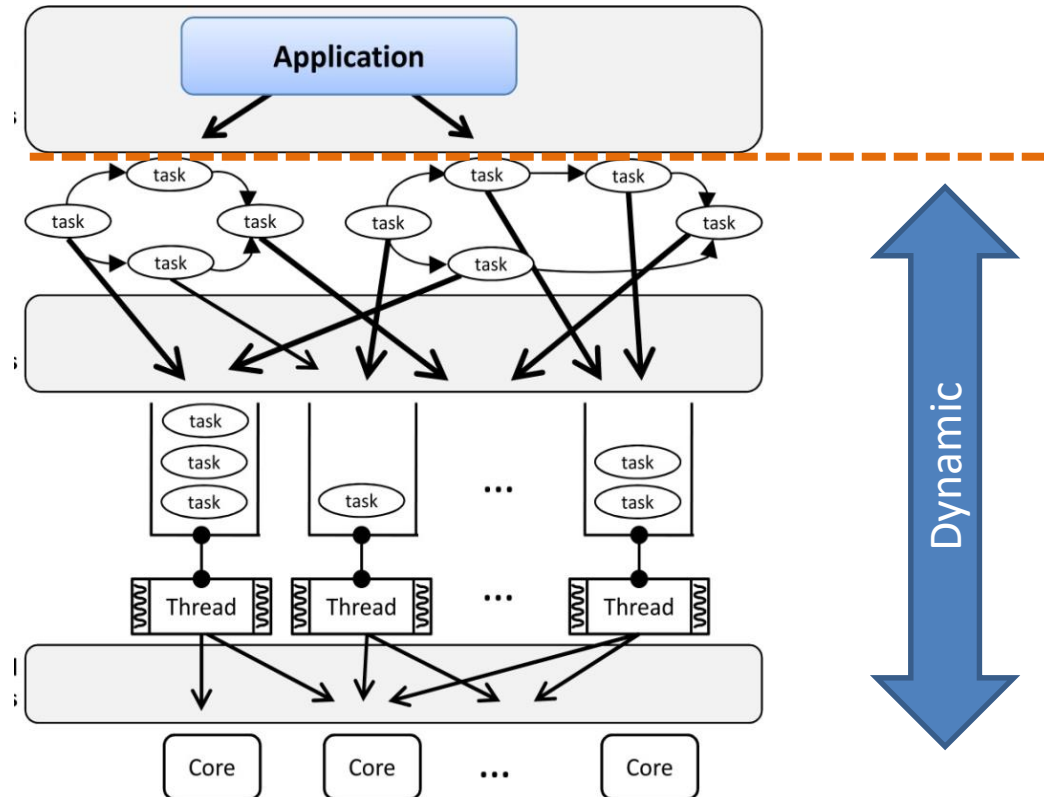
real-time
methodologies
to provide **time
predictability**

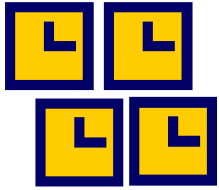


programmability of
many-core from
high-performance computing

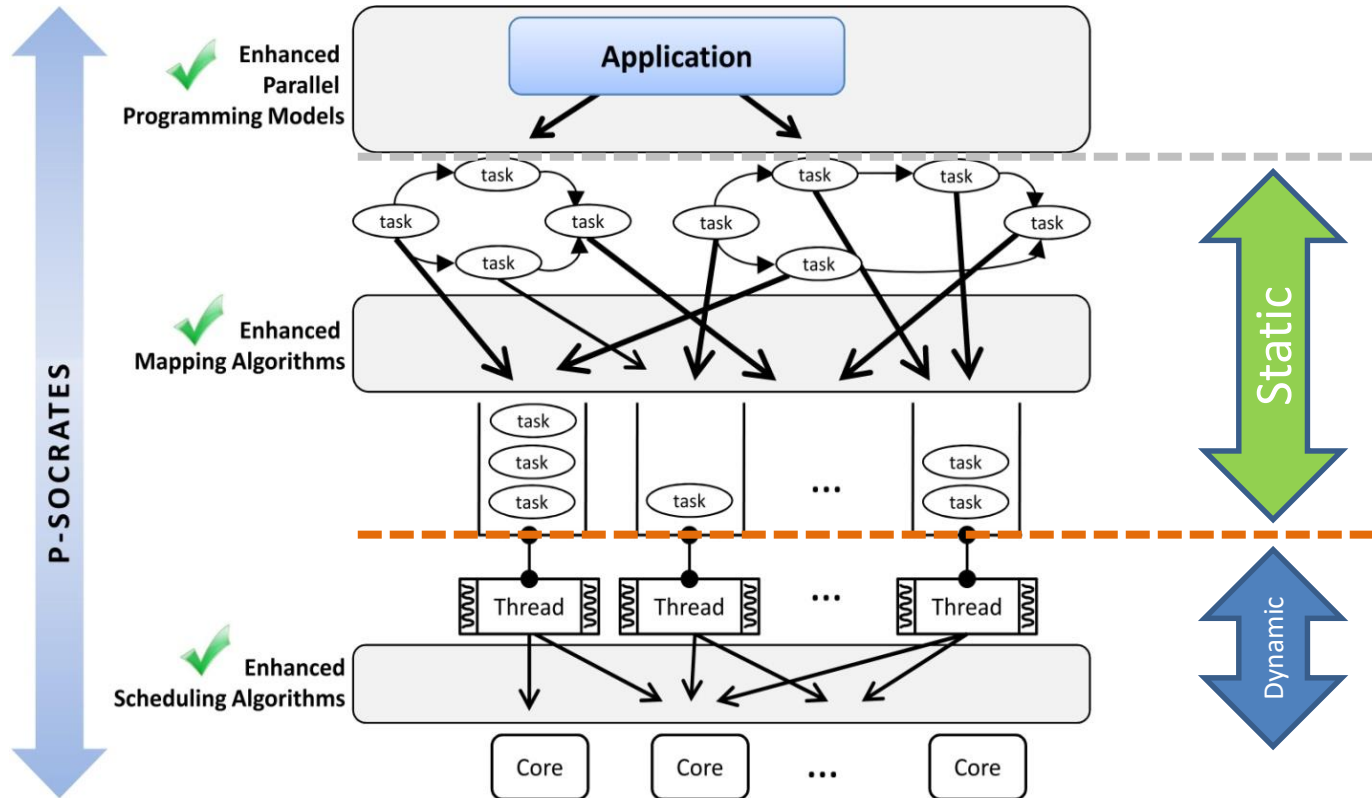


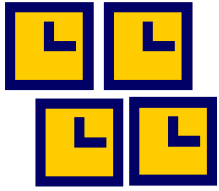
Vision





Vision





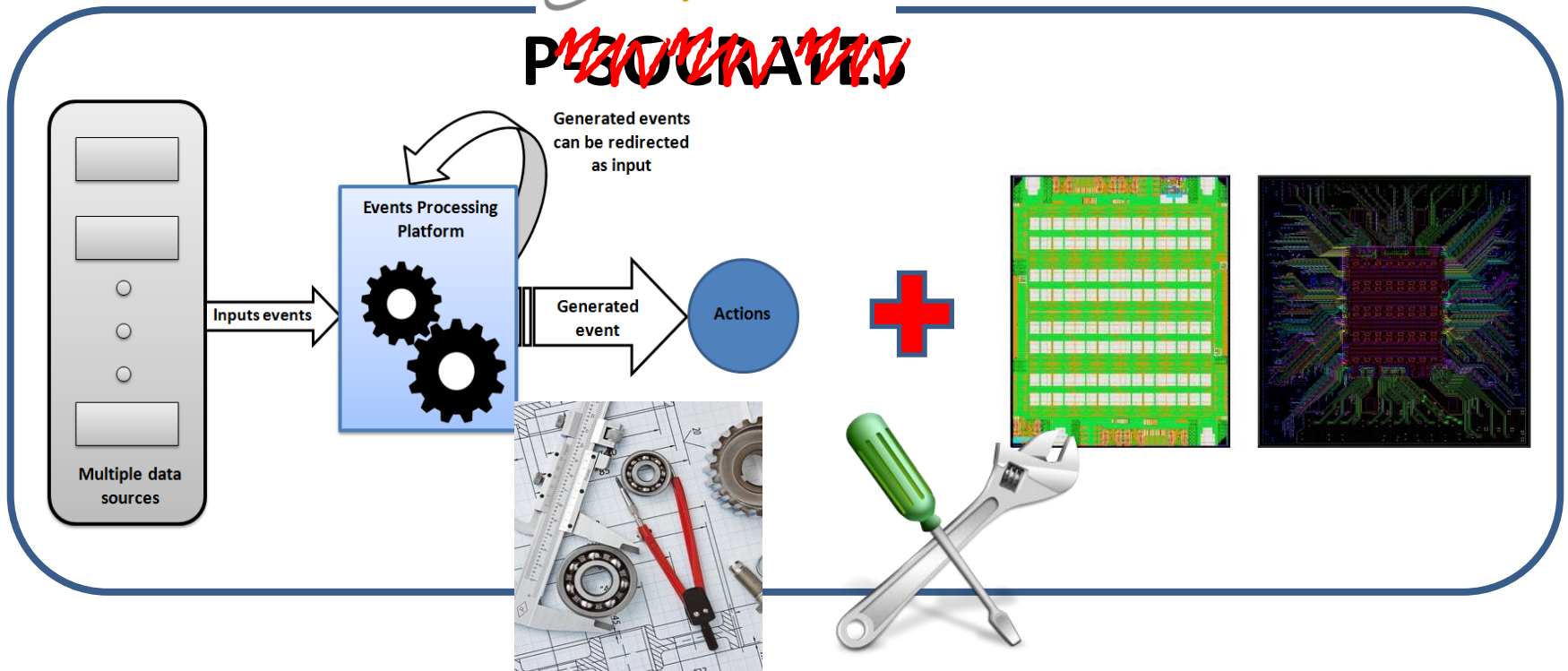
Innovation

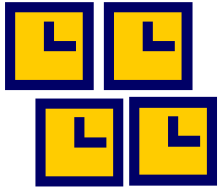
- A **generic framework**, integrating models, tools and system software, to parallel and **real-time** require with **high performance**



~~P-SOCRATES~~

Generated events can be redirected as input

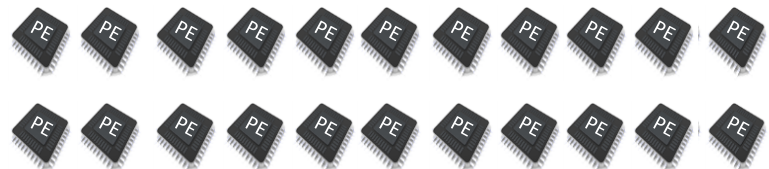
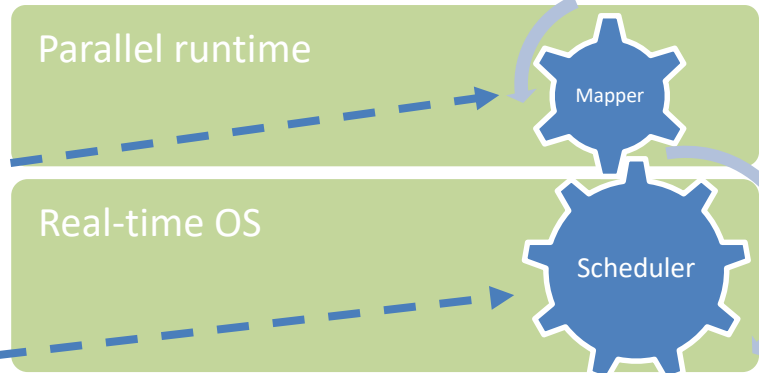
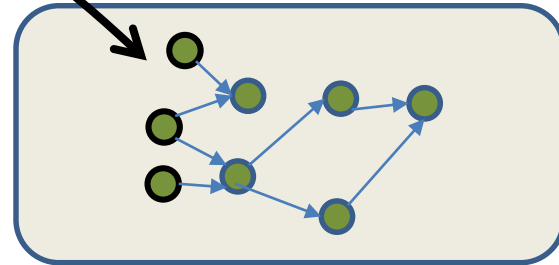




Technical Approach

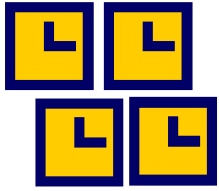
Compiler phase

```
for(int i=0; i<3; i++) {
  for(int j=0; j<3; j++) {
    if(i==0 && j==0) { // Task T1
      #pragma omp task depend(inout:m[i][j])
      compute_block(i, j);
    } else if (i == 0) { // Task T2
      #pragma omp task depend(in:m[i][j-1], inout:m[i][j])
      compute_block(i, j);
    } else if (j == 0) { // Task T3
      #pragma omp task depend(in:m[i-1][j], inout:m[i][j])
      compute_block(i, j);
    } else { // Task T4
      #pragma omp task depend(in:m[i-1][j],m[i][j-1],
                             m[i-1][j-1], inout:m[i][j])
      compute_block(i, j);
    }
  }
}
```



Run-time tracing

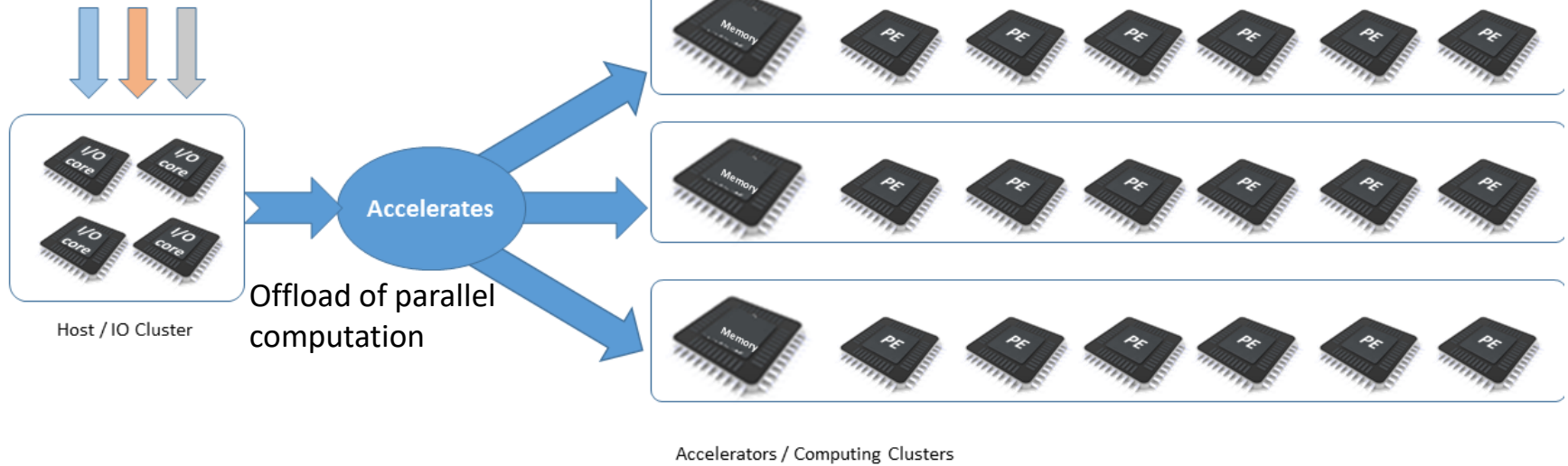
- Exploring Hardware model to guide mapping
- Reducing complexity, grouping
- Scheduling communication/computation
- Explore measurement-based approaches
- Clustered architecture to provide composability

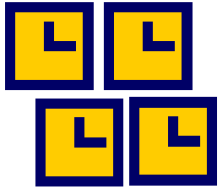


Technical Approach

```
for (int i=0; i<3; i++) {  
  for (int j=0; j<3; j++) {  
    for (int k=0; k<3; k++) {  
      if (i==0 || j==0) { // Task T1  
        #pragma omp task depend(inout:m[i][j])  
        compute_block(i, j);  
      } else if (i == 0) { // Task T2  
        #pragma omp task depend(in:m[i][j-1], inout:m[i][j])  
        compute_block(i, j);  
      } else if (j == 0) { // Task T3  
        #pragma omp task depend(in:m[i-1][j], inout:m[i][j])  
        compute_block(i, j);  
      } else { // Task T4  
        #pragma omp task depend(in:m[i-1][j],m[i][j-1],  
                               m[i-1][j-1], inout:m[i][j])  
        compute_block(i, j);  
      }  
    }  
  }  
}
```

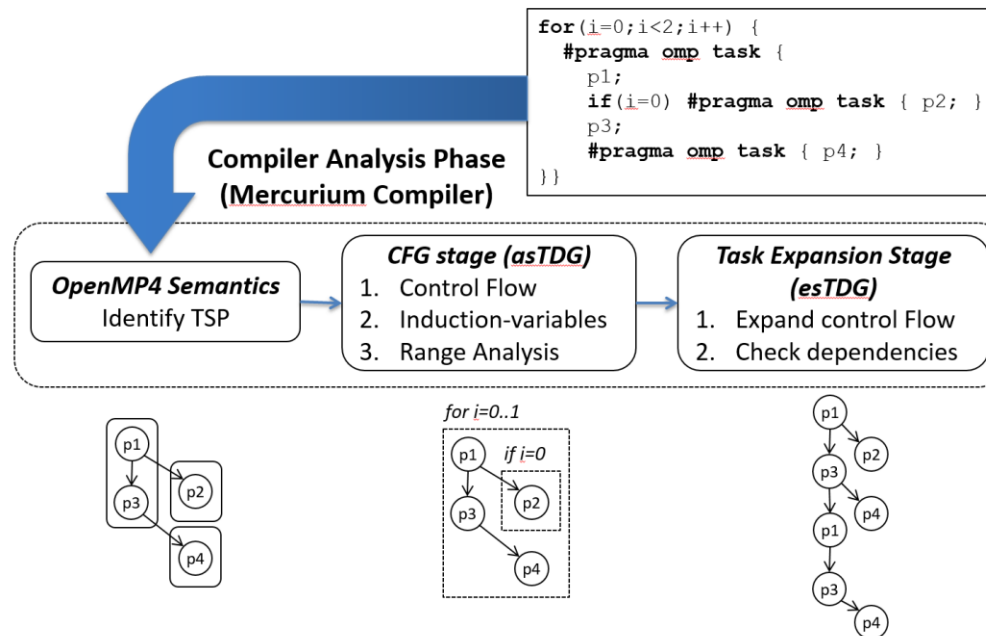
Execution model on heterogenous many-core

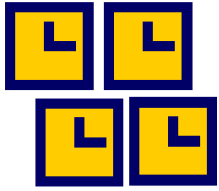




Technical Approach

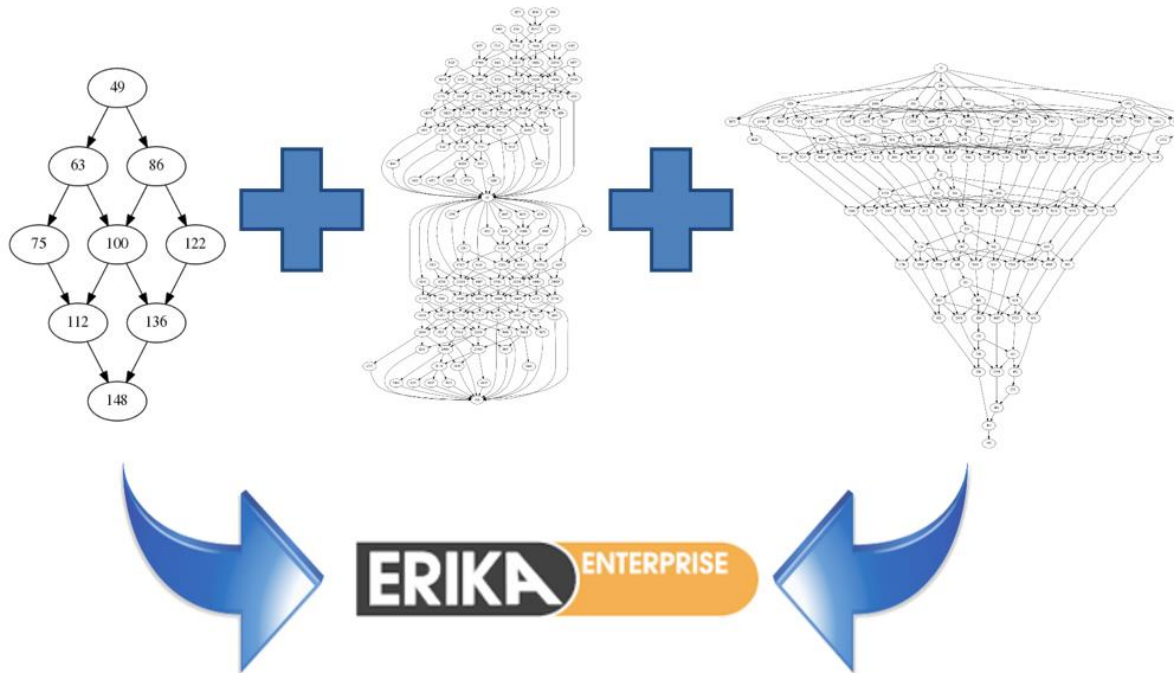
- Extraction of parallelism with data-flow annotations
 - OpenMP tasking semantics generates a graph of control and data flow task execution

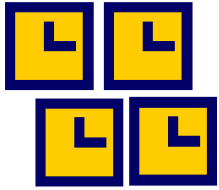




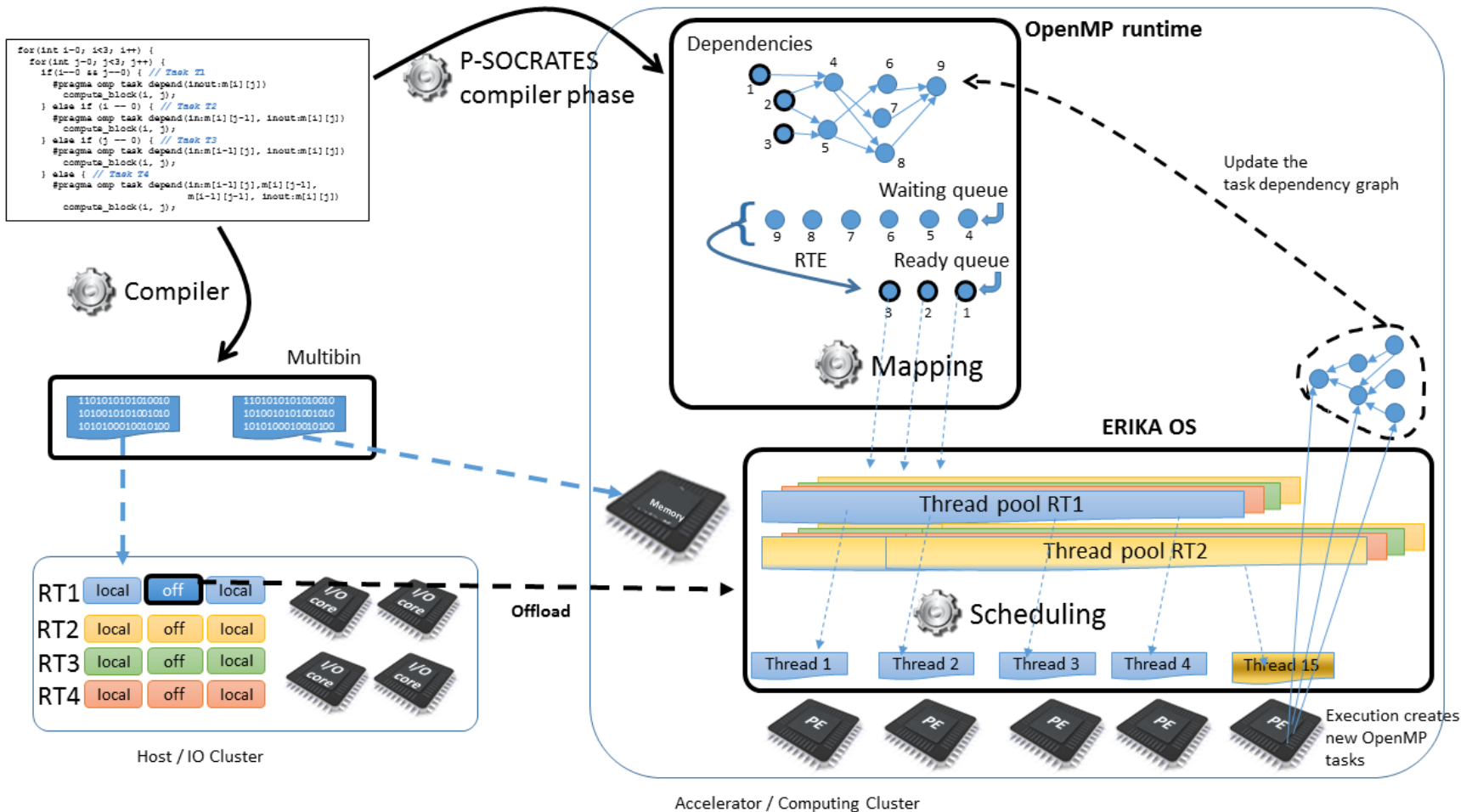
Technical Approach

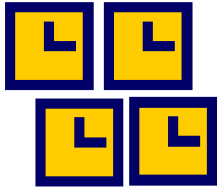
- Scheduler
 - Both static mapping partitioned and dynamic mapping global scheduling approaches





Technical Approach



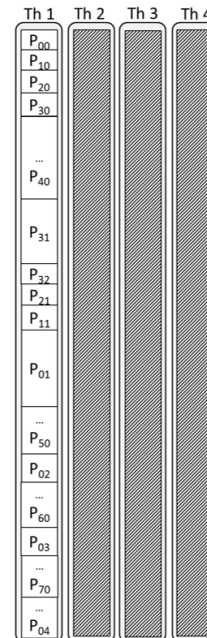


Technical Approach

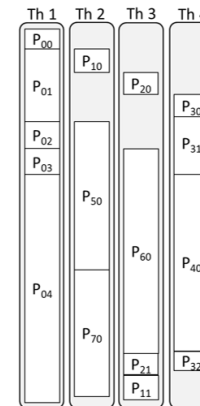
- Schedulability Analysis
 - Schedulability analysis of OpenMP tasking DAGs, considering both tied and untied tasks

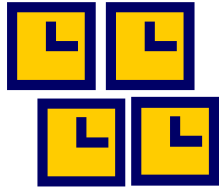
```
1 #pragma omp parallel num_threads(N) {
2 #pragma omp single { // T0
3   part00
4   #pragma omp task { // T1
5     part10
6     #pragma omp task { // T2
7       part20
8       #pragma omp task { // T3
9         part30
10        #pragma omp task { part40 } // T4
11        #pragma omp taskwait
12        part31
13      }
14      #pragma omp taskwait
15      part21
16    }
17    #pragma omp taskwait
18    part11
19  }
20  part01
21  #pragma omp task { part50 } // T5
22  part02
23  #pragma omp task { part60 } // T6
24  part03
25  #pragma omp task { part70 } // T7
26  part04
27 }}
```

tied tasks



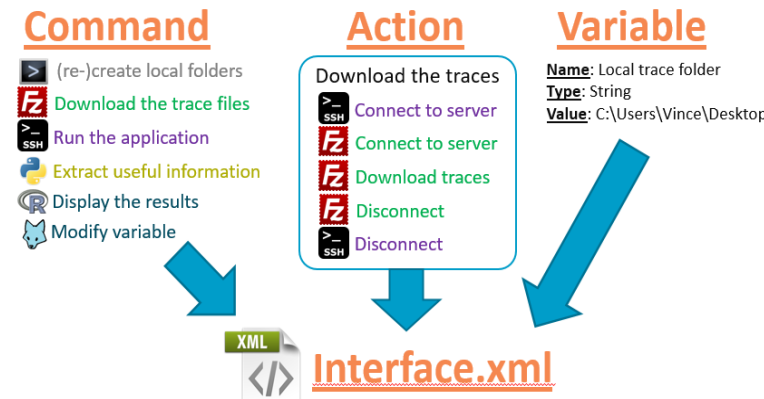
untied tasks

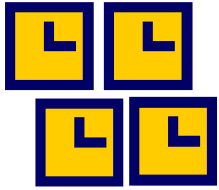




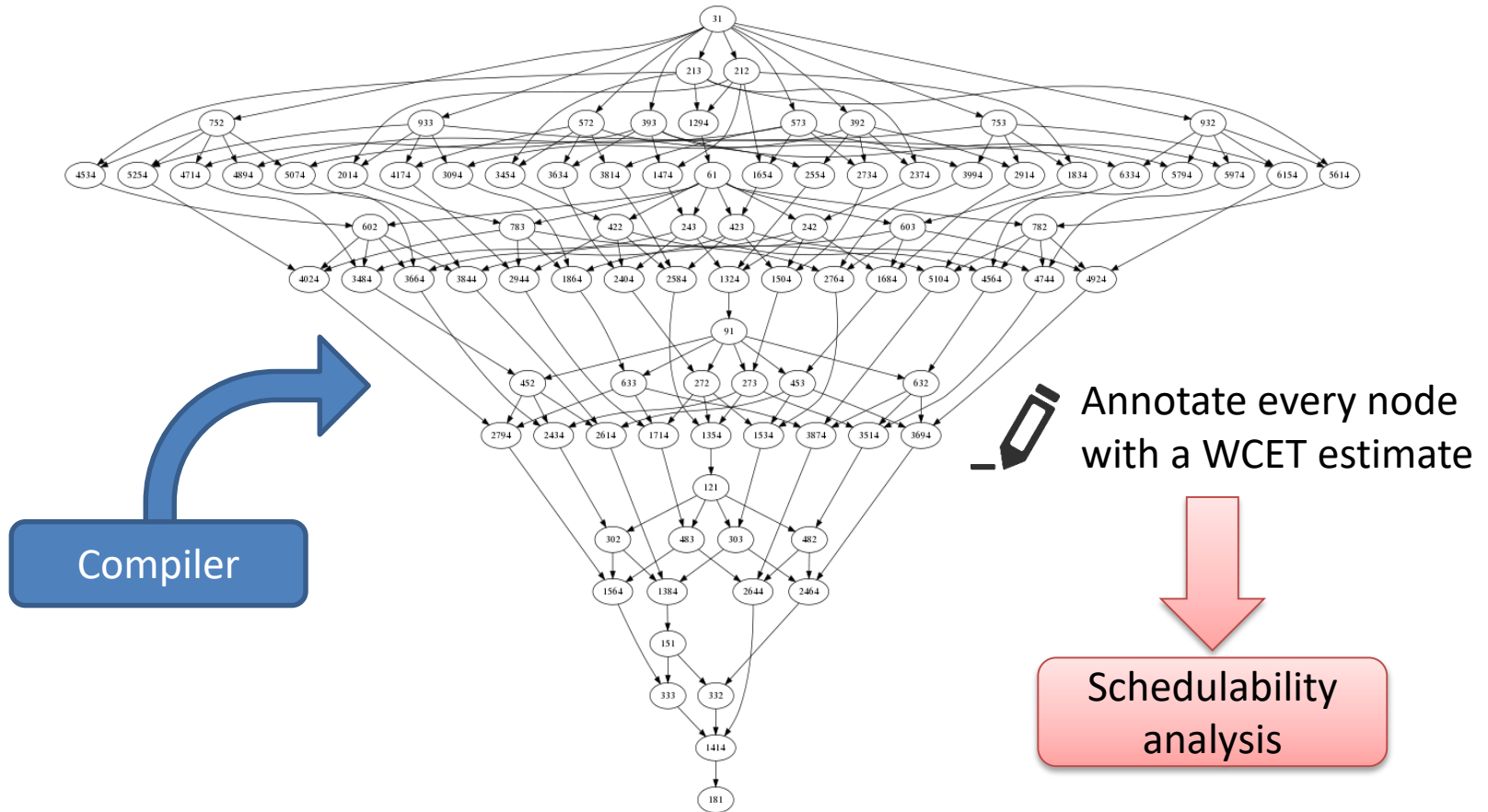
Technical Approach

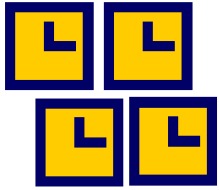
- Timing Analysis
 - Exploring measurement-based approaches
 - Allowing to analyze and reason about an application timing behavior
 - Collecting execution traces is a tedious process
 - Involves many steps, in different languages
 - Developed measurement-based trace collecting and analysis tool
 - Collecting runtime execution traces is fully automatic
 - Extract and compute statistical information from the traces





P-SOCRATES TA Objectives





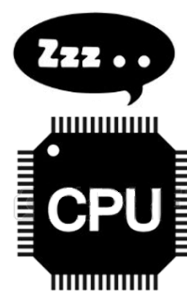
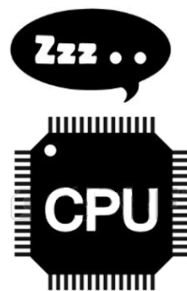
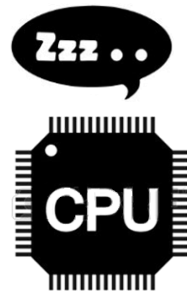
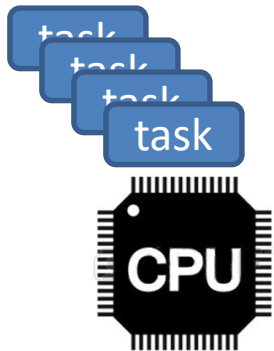
Methodology

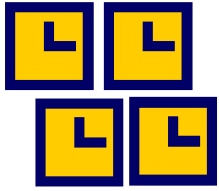
- A new approach to tackle the interference problem

Not one but two WCET estimates



One estimate is obtained by running every task in complete isolation (runs on 1 core, the rest of the system stays quiet)





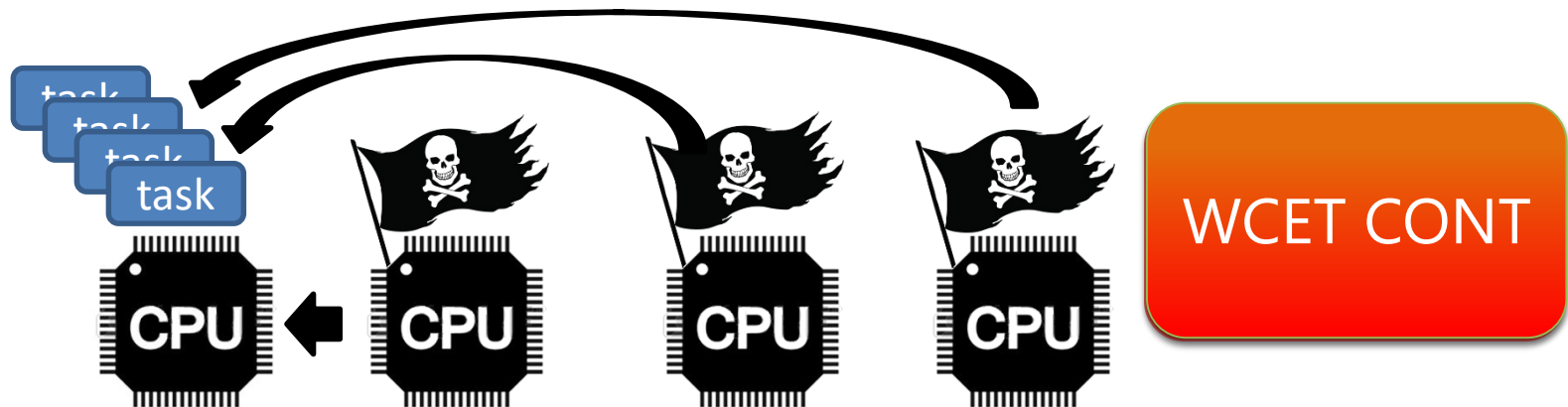
Methodology

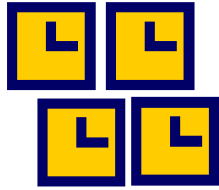
- A new approach to tackle the interference problem

Not one but two WCET estimates

2

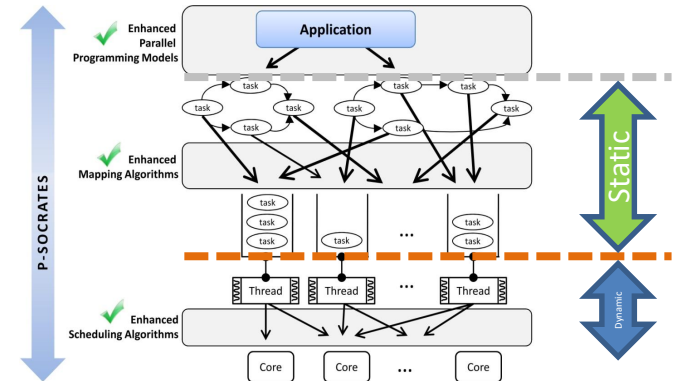
The other is obtained by running every task in complete contention (runs on 1 core, the rest of the system does everything possible to interfere with its execution)

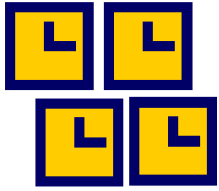




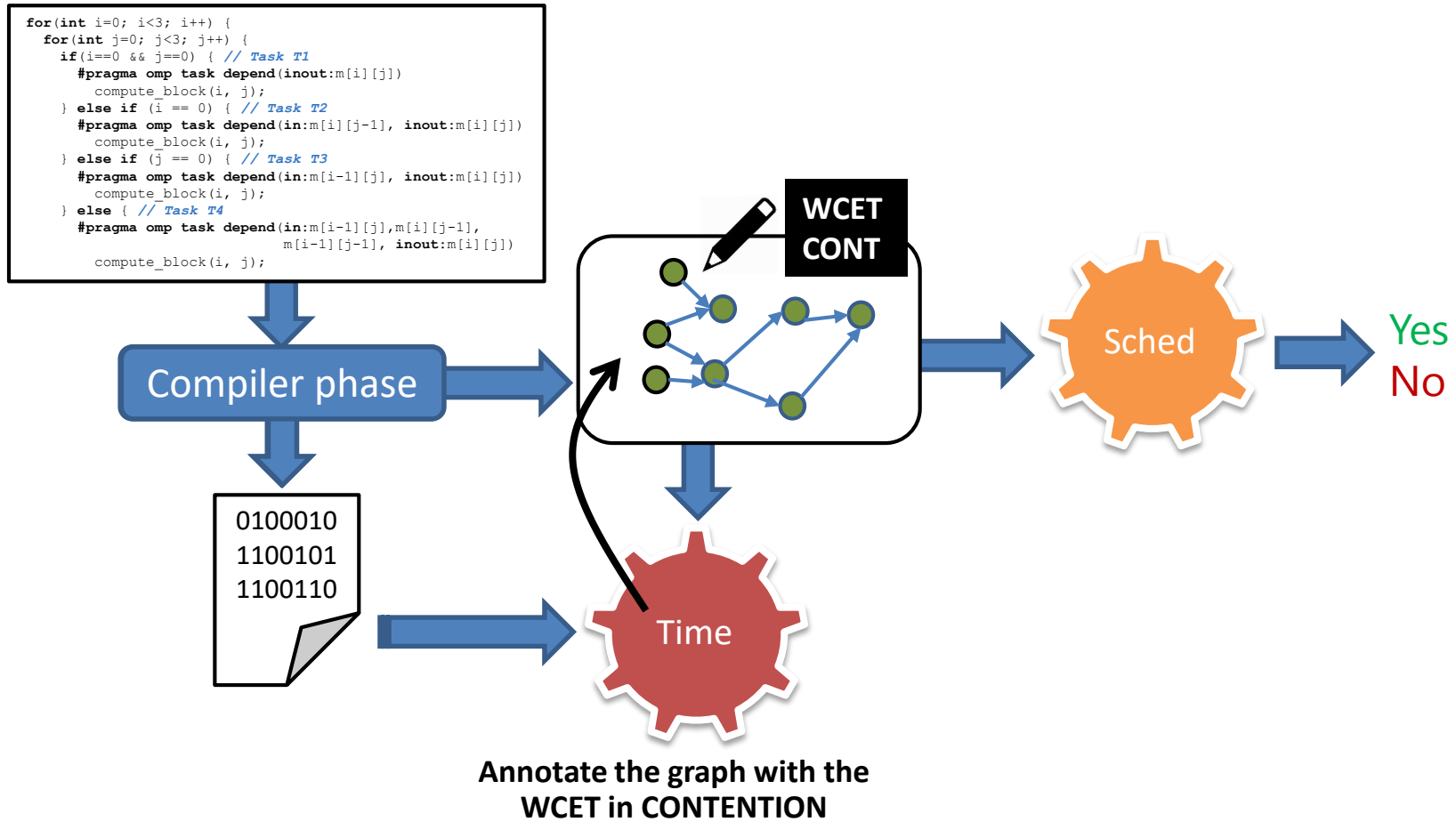
Methodology

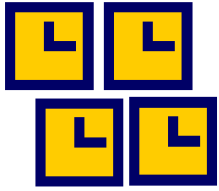
- Processes to perform schedulability analysis
 - Based on both intrinsic and extrinsic WCET estimates
 - One process for the dynamic project approach
 - Task-to-thread mapping is with global queue
 - Thread scheduling is global with limited preemption
 - Maximize average performance
 - Another for the static process approach
 - Fixed task-to-thread mapping (heuristics to minimize makespan)
 - Partitioned per-core scheduling (with limited preemption)
 - Minimize guaranteed response time



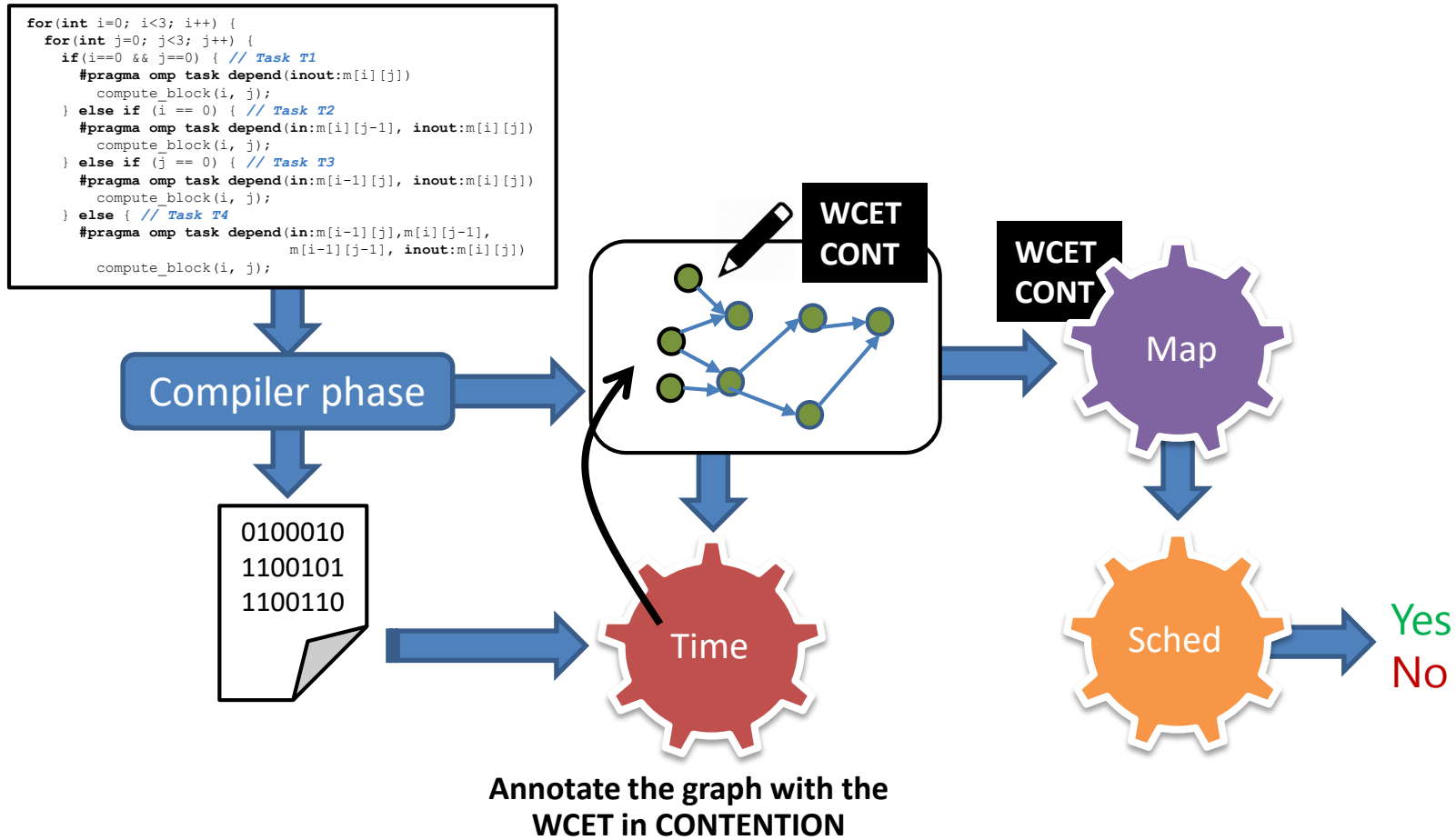


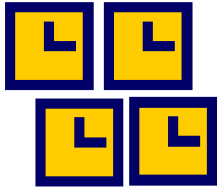
The big picture (dynamic)



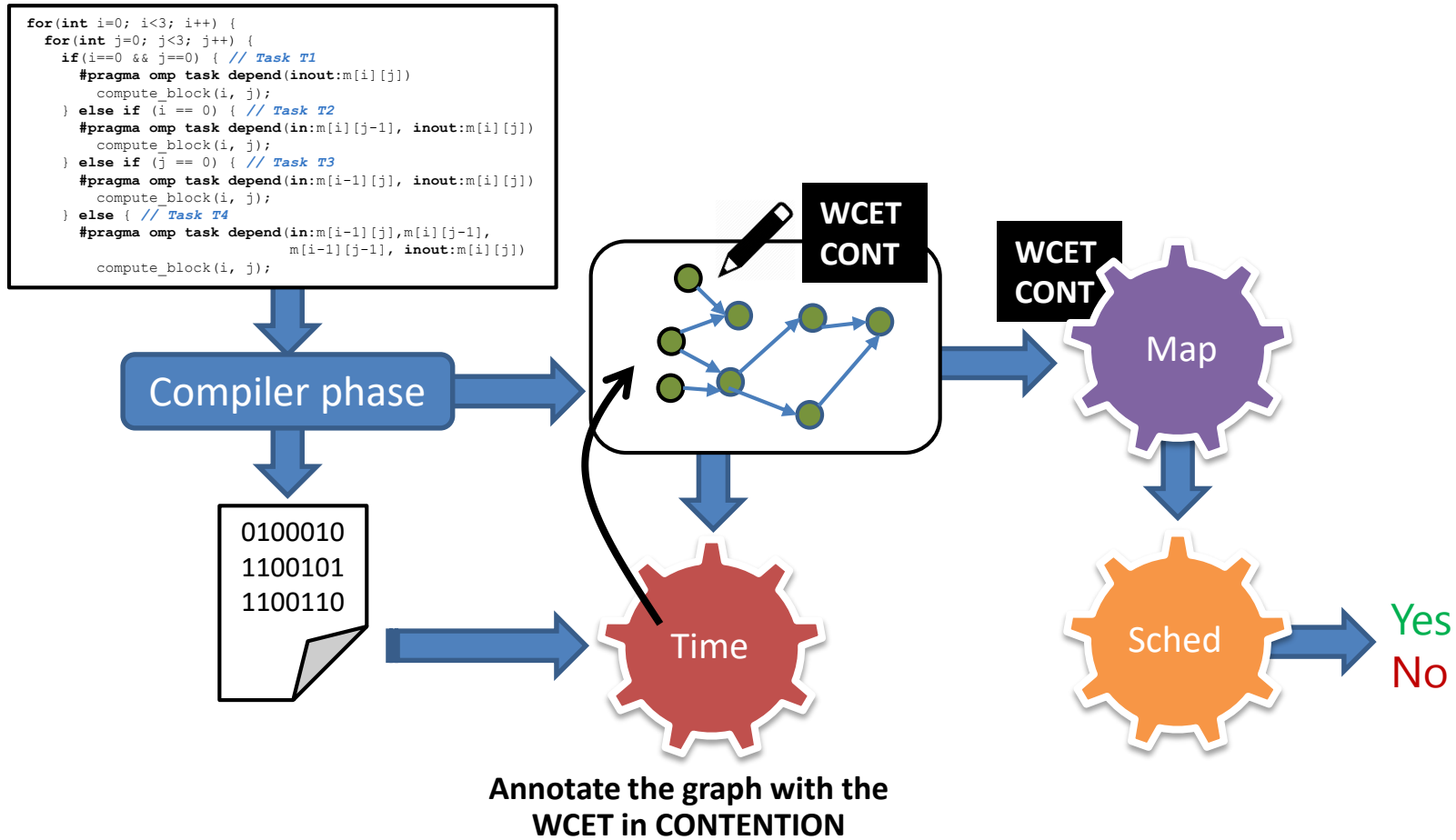


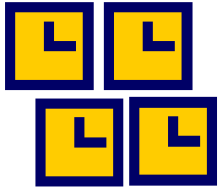
The big picture (static)



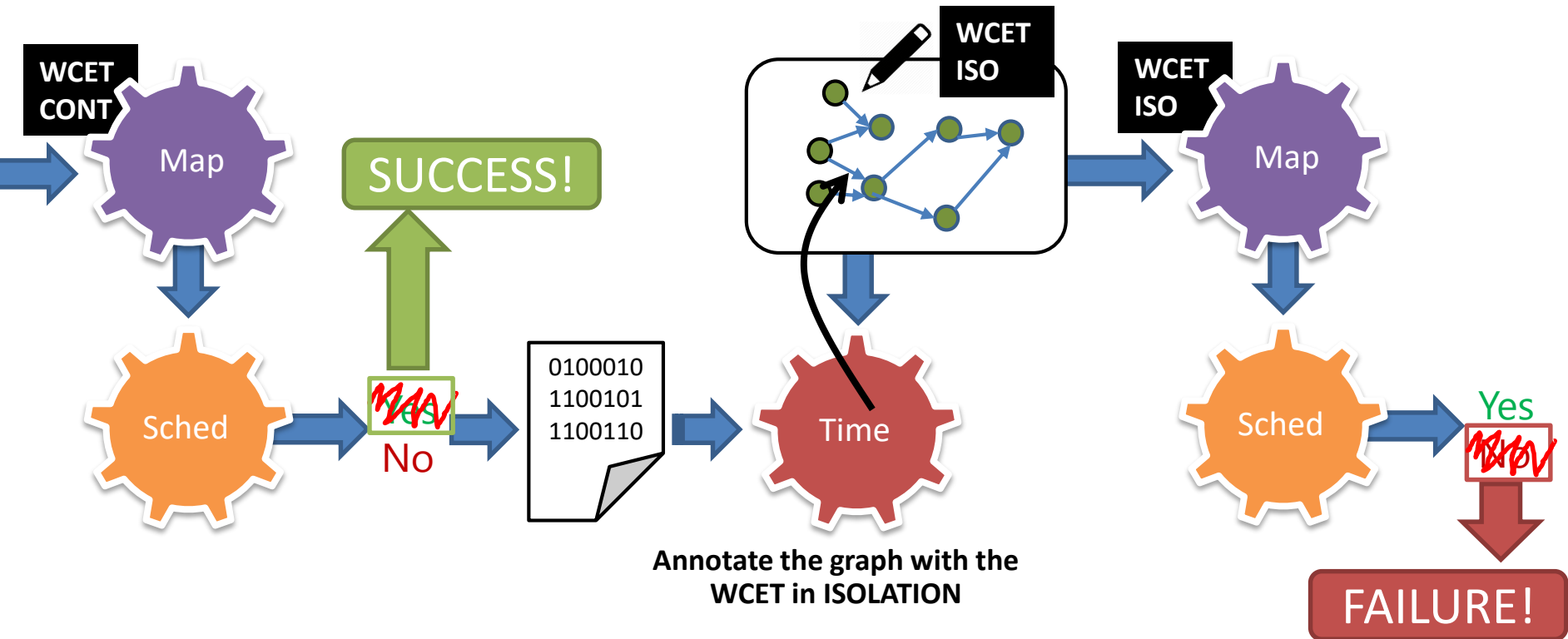


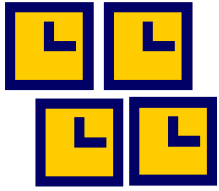
The big picture (static)



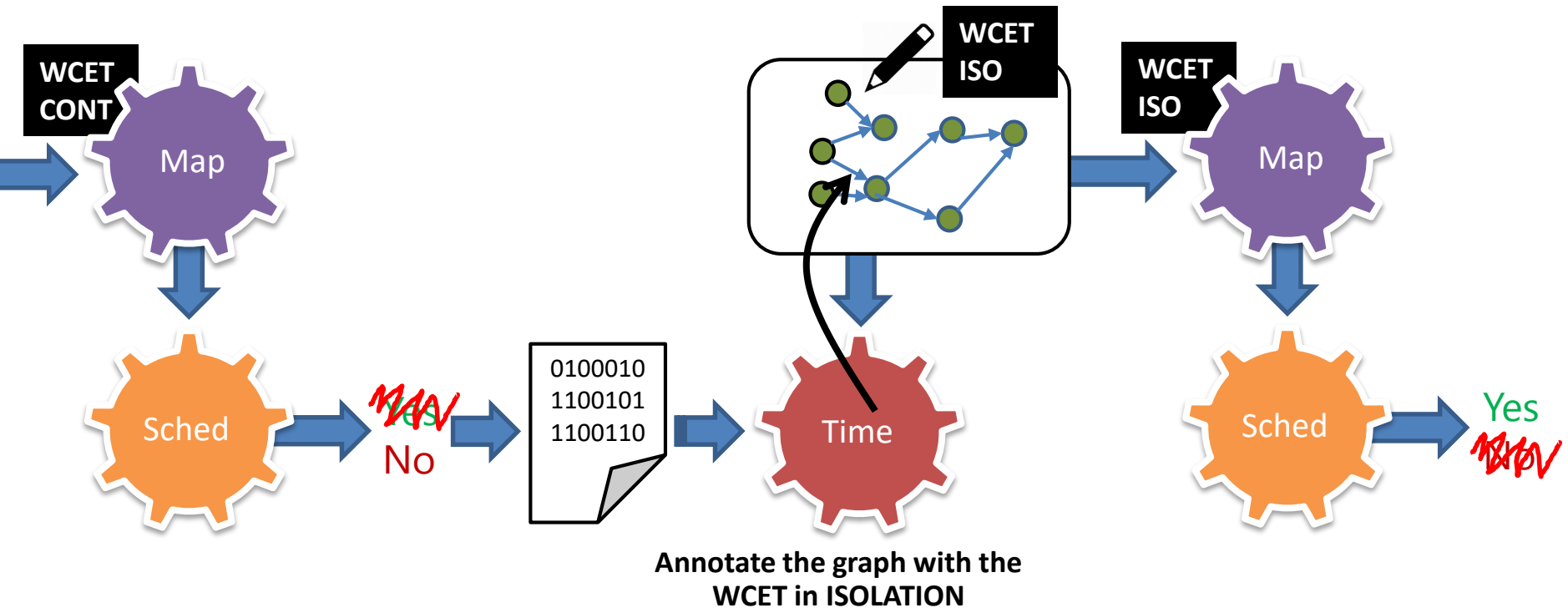


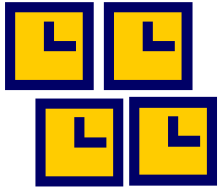
The big picture (static)



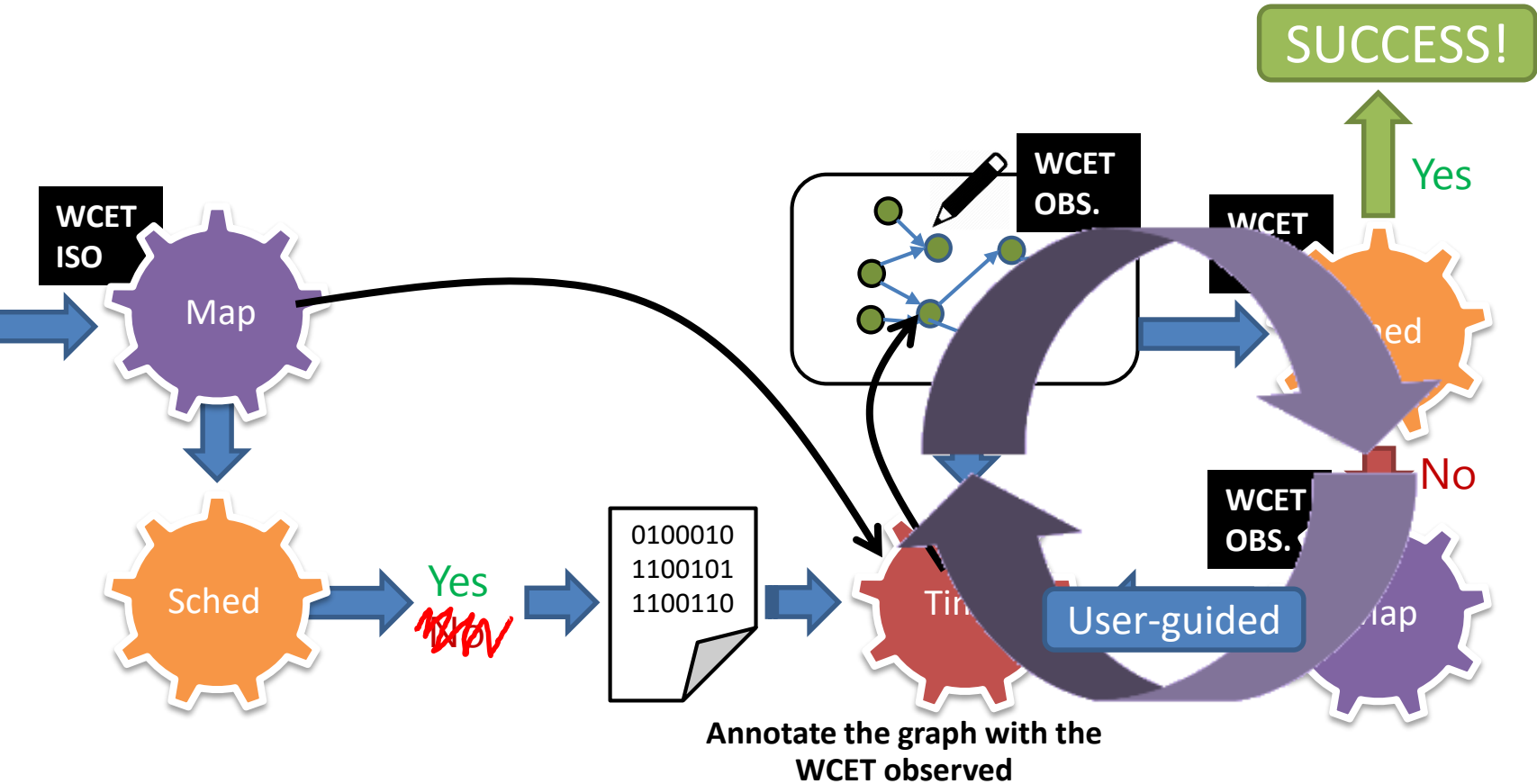


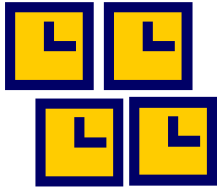
The big picture (static)





The big picture (static)





All-in-one

Variables Commands Actions Console Options

+ New action

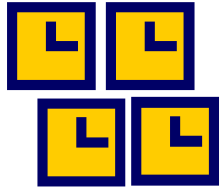
List of actions:

- temp iso analysis
- temp contention analysis
- temp normal analysis
- Annotate TDG
- upload code
- (static) Extract MIET and MEET
- (static) 2. Derive mapping based on MEET
- (static) 3. Derive mapping based on MIET
- (static) 4. Extract MAET
- (static) 5. Derive mapping based on MAET
- (dynamic) Compile and Run
- (static - given) Compile and Run
- (static - MEET) Compile and Run
- Dynamic vs. Static

List of commands of 'Dynamic vs. Stat [...]' (click to fold actions)

1. Connect to the MPPA with BSC account
2. Recompile Erika with dynamic scheduler
3. Close the active SSH connection
4. Connect to the MPPA with PSOC account
5. Copy Erika to the shared folder
6. Close the active SSH connection
7. Connect to the MPPA through SSH
8. Open a SFTP connection with the MPPA
9. Set mode to dynamic
10. Create the local result directory
11. (Re)create all the remote directories
12. Clear out all the remote directories
13. Upload all the source codes
14. Compile the main cluster file and extract the TDG information
15. Boxer
16. Generate a dynamic mapping
17. Compile the source code of the IO application
18. Compile the source code of the cluster application
19. Create the multibinary
20. Run the multibinary

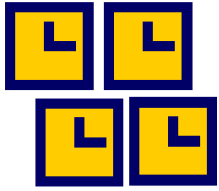
Action "Dynamic vs. Static" displayed.



Use-cases

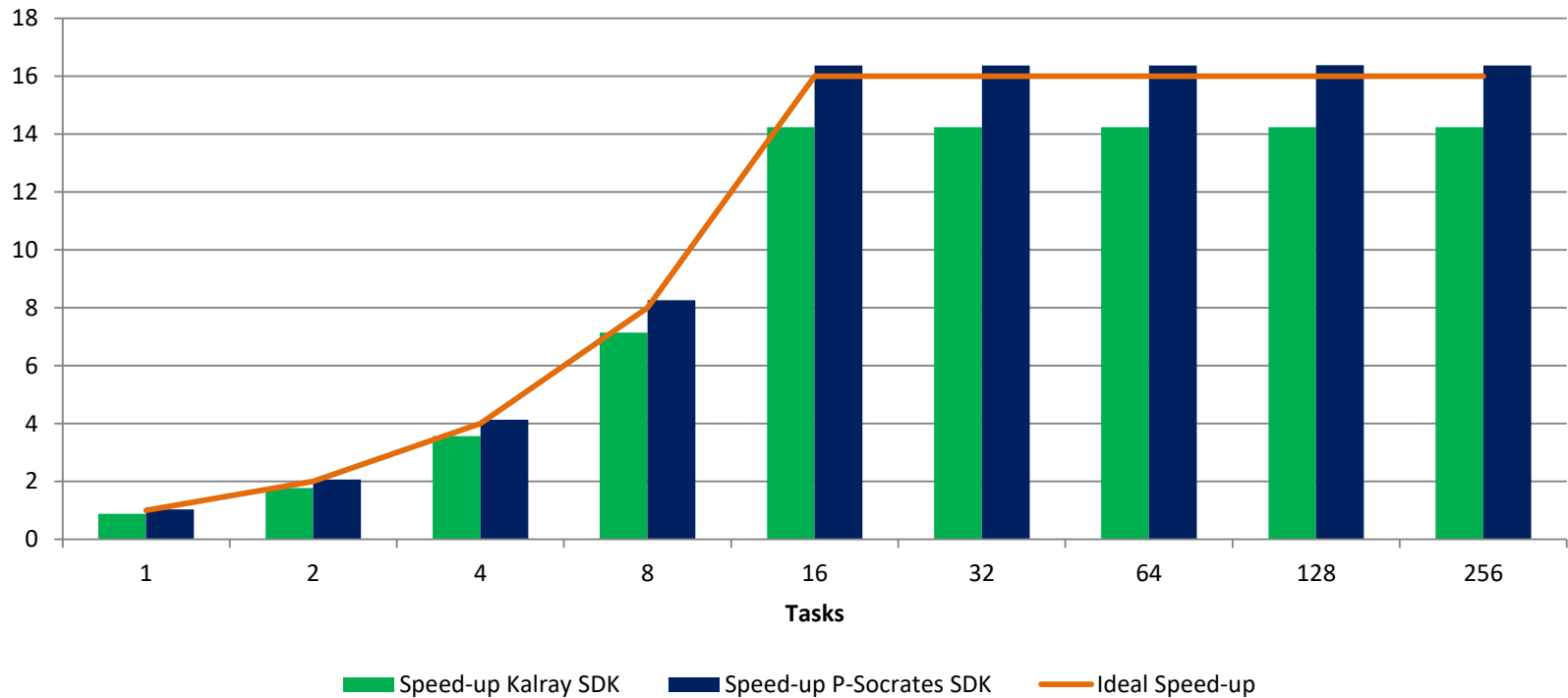
- Intelligent Traffic Application
 - Complex event processing engine for public transport
- Space Case Study
 - Pre-processing application for infrared detectors used for the Ecluid space mission
- Online Text Semantics
 - Tool performs semantic analysis, categorizes and extracts information from text
- All case studies will execute on a COTS processor
 - Kalray MPPA Bostan



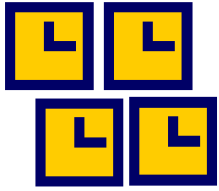


Results

- Intelligent traffic application

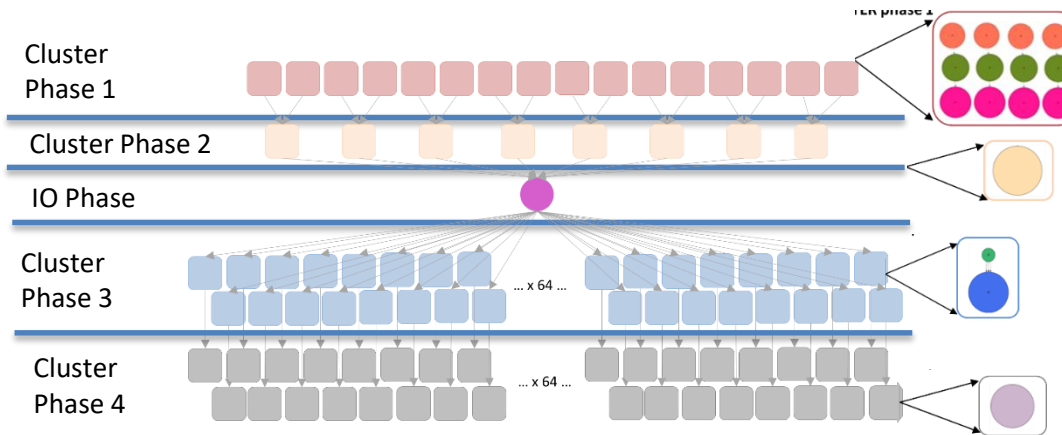


Complex Function → set of 256ffts (size=512)

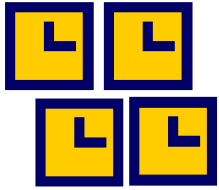


Results

- Infra-red image processing

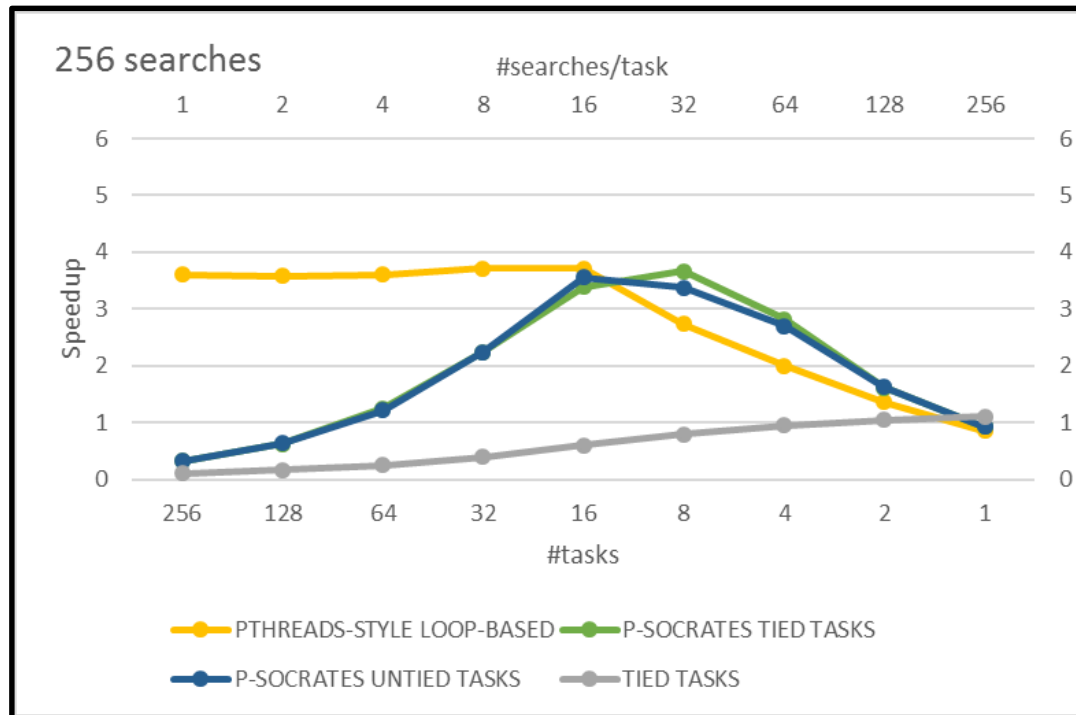


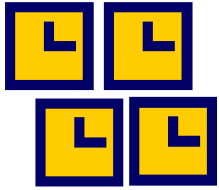
	Execution time (ms)			Speed-up		
	Sequential	MPPA Native SDK	P-SOCRATES SDK	MPPA Native SDK	P-SOCRATES SDK	Maximum theoretical
TOTAL	63140	8872,4	9093,2	7,1	6,9	256
<i>Cluster Phase 1</i>	2710	162,64	150,34	16,7	18,0	48
<i>Cluster Phase 2</i>	562	147,56	120,08	3,8	4,7	8
<i>IO Phase</i>	612	612	612	1,0	1,0	1
<i>Cluster Phase 3</i>	8554	804,9	879,1	10,6	9,7	16
<i>Cluster Phase 4</i>	950	236,9	285,6	4,0	3,3	16



Results

- Online text semantics

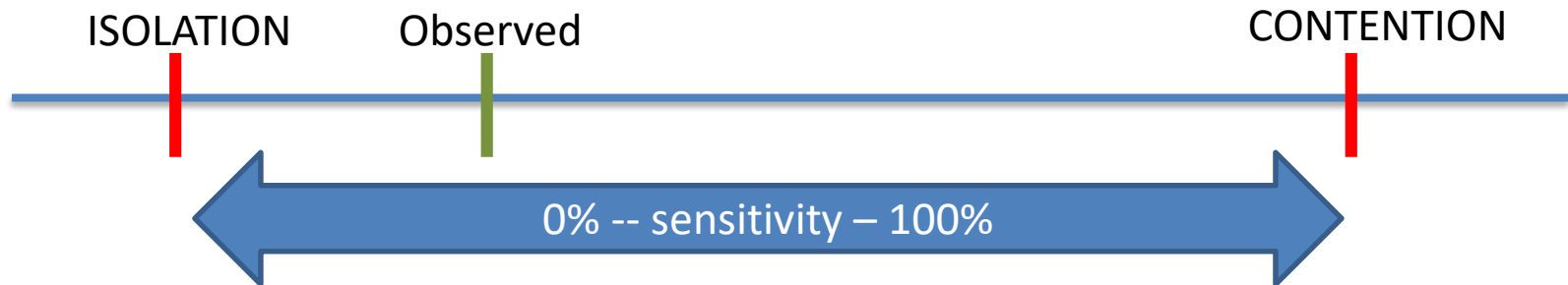


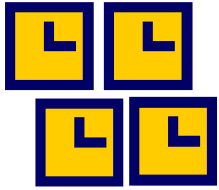


Results

- Online text semantics

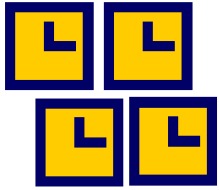
ISOLATION (μ s)	Observed (μ s)	CONTENTION (μ s)	Core	sensitivity
102	98	3 445	7	0%
98	241	3 490	6	4%
77	242	2 715	5	6%
89	238	2 463	4	6%
68	239	1 471	3	12%
68	239	1 478	2	12%
78	239	3 498	1	4%
72	71	3 484	0	0%





Conclusions

- Integrating time-predictability in high-performance embedded computing brings difficult challenges that need to be addressed
 - High-performance hardware and software stacks are not designed for predictability.
- The P-SOCRATES project tackled this important challenge by devising a methodology and the UpScale SDK
 - Allows to reason on the timing and schedulability analysis of real-time high-performance applications.
- The dynamic configuration approach achieves the same average performance than the default SDK
 - Static approach achieves higher **Guaranteed Performance**, with similar average performance (~10%)



Thank you



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